



F. I. M. E.

ELECTRONICA LOGICA I

ING JUAN ANGEL GARZA GARZA

HORA: V-6

Martha Patricia Razo Cruz

MAT. 385980

ELECTRÓNICA LÓGICA I

ING. JUAN ANGEL GARZA GARZA. 191002 *

- I.- INTRODUCCION
- II.- SISTEMAS NUMERICOS
- III.- ALGEBRA BOOLEANA
- IV.- CODIGO Y REPRESENTACION DE INFORMACION
- V.- MINIMIZACION DE FUNCIONES BOOLEANAS
- VI.- DISEÑO COMBINACIONAL
- VII.- FLIPFLOPS (ELEMENTOS DE MEMORIA)
- VIII.- DISEÑO SECUCIAL
- IX.- FAMILIAS LOGICAS

TEXTO: FUNDAMENTOS DE DISEÑO DIGITAL

AUTOR INGENIERO CESAR LEAL CHAPA

ALUMNO. MARTHA PATRICIA RAZO CRUZ

MATRICULA. 385980

HORA.

■ V-6 ■

* SISTEMAS NUMERICOS *

NUMEROS ROMANOS

I - 1	C - 100
II - 5	D - 500
X - 10	M - 1000
L - 50	

SISTEMA NUMERICO DECIMAL

0	4	8
1	5	9
2	6	
3	7	

FORMULA GENERAL

$$N = \sum_{i=0}^{i=n-1} a_i R^i$$

N = CANTIDAD

i = POSICION

a = COEFICIENTE

n = NUMERO DE DIGITOS

R = RAIZ O BASE DEL SISTEMA

BINARIO

0, 1

OCTAGONAL

0, 1, 2, 3, 4, 5

6, 7

HEXADECIMAL

0, 1, 2, 3, 4, 5

6, 7, 8, 9, A, B,

C, D, E, F.

EJEMPLO 529(10)

$$N = 9(10)^0 + 2(10)^1 + 5(10)^2 \rightarrow 9 + 20 + 500$$

$$N = \underline{529} *$$

N(10)	N(2)	N(8)	N(16)	N(3)
0	0000	0	0	0
1	0001	1	1	1
2	0010	2	2	2
3	0011	3	3	10
4	0100	4	4	11
5	0101	5	5	12
6	0110	6	6	20
7	0111	7	7	21
8	1000	10	8	22
9	1001	11	9	100
10	1010	12	A	101
11	1011	13	B	102
12	1100	14	C	110
13	1101	15	D	111
14	1110	16	E	112
15	1111	17	F	120
16	10000	20	10	121

* CONVERSIONES *

- $N(x) \rightarrow N(10)$ {
 - a) FORMULA GENERAL $N(10) = \sum_{i=0}^{(N-1)} a_i R^i$
 - b) MULTIPLICAR POR LA BASE Y SUMAR
- $N(10) \rightarrow N(x)$ {
 - a) EXTRACCION DE POTENCIAS
 - b) RESIDUOS

- $N(2) \leftrightarrow N(8)$ {
 - MULTIPLICADO
- $N(2) \leftrightarrow N(16)$ {
 - MULTIPLICADO

EJEMPLOS:

① $N(2) \rightarrow N(10)$

* $N(x) \rightarrow N(10)$ *

a) FORMULA GENERAL $10111011(2) = N(10)$

MSB \nearrow \nwarrow LSB

$$N(10) = \sum_{i=0}^{i=N-1} a_i R^i \quad N(10) = 1(2)^0 + 1(2)^1 + 0(2)^2 + 1(2)^3 + 1(2)^4 + 1(2)^5 + 1(2)^6 + 1(2)^7$$

BIT = DIGITO BINARIO $N(10) = 1 + 2 + 8 + 16 + 32 + 128 = 187$

BYNARY DIGIT $N(10) = 187$

LSB = BIT DE MENOR PESO $10111011(2) \Rightarrow 187(10)$ *

MSB = BIT DE MAYOR PESO

$$\textcircled{2} N(8) \rightarrow N(10)$$

$$27.32(8) \rightarrow N(10)$$

$$N(10) = 2(8)^3 + 7(8)^2 + 3(8)^1 + 2(8)^0$$

$$N(10) = 16 + 7 + \frac{3}{8} + \frac{2}{64}$$

$$N(10) = 23.40625$$

$$\underline{27.32(8) = 23.40625(10)} \quad *$$

$$\textcircled{3} N(16) \rightarrow N(10)$$

$$ABD(16) = N(10)$$

$$A = 10$$

$$B = 11 \quad N(10) = 10(16)^2 + 11(16)^1 + 13(16)^0$$

$$C = 12 \quad N(10) = 2560 + 176 + 13$$

$$D = 13 \quad N(10) = 2749$$

$$E = 14 \quad \underline{2749(16) = ABD(10)} \quad *$$

$$F = 15$$

$$\textcircled{4} N(9) \rightarrow N(10)$$

$$N(10) = 3(9)^2 + 7(9)^1 + 6(9)^0 \rightarrow 243 + 63 + 6$$

$$N(10) = 312$$

$$376(9) \rightarrow N(10)$$

$$\underline{312(10) = 376(9)} \quad *$$

$$* N(x) \rightarrow N(10) *$$

b).- MULTIPLICAR POR LA BASE Y SUMAR: ESTE METODO CONSISTE EN MULTIPLICAR EL (MSB) POR LA BASE Y EL PRODUCTO RESULTANTE SUMARLO CON EL BIT SIGUIENTE A LA DERECHA, EL RESULTADO SE MULTIPLICA DE NUEVO POR LA BASE Y SE SUMA CON EL BIT O DIGITO SIGUIENTE Y ASI SUCEDE HASTA LLEGAR AL (LSB) EL RESULTADO DE TODAS ESTAS OPERACIONES RESULTA SER EL EQUIVALENTE DECIMAL.

EJEMPLO:

$$\textcircled{1} N(2) = N(10)$$

$$10111011(2) = N(10)$$

$$1(2) + 0 = 2 \quad 23(2) + 0 = 46$$

$$2(2) + 1 = 5 \quad 46(2) + 1 = 93$$

$$5(2) + 1 = 11 \quad 93(2) + 1 = 187$$

$$11(2) + 1 = 11 \quad \underline{187(10) = 10111011(2)} \quad *$$

$$\textcircled{2} N(8) \rightarrow N(10)$$

$$325(8) = 213(10)$$

$$\textcircled{4} N(16) \rightarrow N(10)$$

$$A2C(16) \rightarrow 2604(10)$$

$$\textcircled{3} N(9) \rightarrow N(10)$$

$$347(9) = 286(10)$$

BINARIO HEXAGESIMAL

* $N(10) \rightarrow N(x)$ *

a). - EXTRACCION DE POTENCIAS

①

$$35(10) \Rightarrow N(8)$$

$$\begin{array}{r} 8^0 = 1 \\ 8^1 = 8 \\ 8^2 = 64 \end{array} \left. \begin{array}{l} 35 \\ -8 \rightarrow 8^1 \\ \hline 27 \\ -8 \rightarrow 8^1 \\ \hline 19 \\ -8 \rightarrow 8^1 \\ \hline 11 \\ -8 \rightarrow 8^1 \\ \hline 3 \\ -3 \text{ VECES } 8^0 \rightarrow 3^0 \\ \hline 0 \end{array} \right\} 4^1$$

$35(10) = 43(8)$ *

②

$$467(10) = N(8)$$

$$\begin{array}{r} 467 \overline{) 6} \\ 450 \overline{) 5} \\ \hline 17 \overline{) 6} \\ \hline 16 \overline{) 6} \\ \hline 1 \end{array}$$

$467(10) = 5332(8)$ *

③

$$35(10) = N(2)$$

$$\begin{array}{r} 35 \overline{) 2} \\ 17 \overline{) 2} \\ \hline 18 \overline{) 2} \\ \hline 16 \overline{) 2} \\ \hline 2 \overline{) 2} \\ \hline 0 \end{array}$$

$35(10) = 100011(2)$ *

④

$$35(10) = N(16)$$

$$\begin{array}{r} 35 \overline{) 16} \\ 32 \\ \hline 3 \end{array}$$

$33(16) = 35(10)$ *

⑤

$$187(10) = N(16)$$

$$\begin{array}{r} 187 \overline{) 16} \\ 176 \\ \hline 11 \end{array}$$

$BB(16) = 187(10)$ *

⑥

$$N(8) \rightarrow N(10)$$

$$273(8) \rightarrow N(10)$$

$273(8) = 187(10)$

$N(x) \rightarrow N(10) \Rightarrow$ MULTIPLICAR POR LA BASE Y SUMAR

$N(10) \rightarrow N(x) \Rightarrow$ RESIDUOS

$N(x) \rightarrow N(4)$

$N(8) \rightarrow N(8)$ } MULTIPLO

$N(2) \rightarrow N(16)$

* CADA TRES BITS CONVERTIDOS A DECIMALES FORMAN UN DIGITO EN OCTAL

$$\underline{101100011010}_2 \Rightarrow N(8)$$

$$\begin{array}{cccc} 5 & 4 & 3 & 2 \end{array} (8)$$

$$205(8)$$

$$\begin{array}{cccc} \uparrow & \uparrow & \uparrow & \\ 10 & 000 & 101 & \end{array}$$

$$2 \quad 1 \quad 0$$

$$7 \quad 4 \quad X$$

$$4 \quad 2 \quad 1$$

③ $10101111101_2 = N_2$
 $1\ 2\ 7\ 7\ 5\ 8$

④ $6215_8 = N_8$
 11001000101_2

* PARA 4 BIT CONVERTIDOS A DECIMAL REPRESENTAN UN DIGITO EN HEXAGESIMAL. EJEMPLOS:

① $10011011010010_2 = N_{16}$
 $9\ A\ D\ 2\ (16)$

⑤ $34F3_{16} = N_{16}$
 $0010\ 1010\ 1111\ 0011_2$

② $2A73FC_{16} = N_{16}$
 $0010\ 1010\ 0111\ 0011\ 1111\ 1100_2$

0010101011110011_2
 $2\ 5\ 3\ 6\ 3\ 8$

S U M A *

$$\begin{array}{r} 297_{10} \\ + 345_{10} \\ \hline 642_{10} \end{array}$$

$$\begin{array}{r} 10111_2 \\ + 101011_2 \\ \hline 1011010_2 \end{array}$$

$$\begin{array}{r} 1111_2 \\ 1011_2 \\ + 1110_2 \\ + 1110_2 \\ 111_2 \\ \hline 1011_2 \\ 111010_2 \end{array}$$

$$\begin{array}{r} 11111_2 \\ 11111_2 \\ + 11111_2 \\ 101111_2 \\ \hline 101100_2 \\ 100010110_2 \end{array}$$

$$\begin{array}{r} 237_8 \\ + 654_6 \\ \hline 1144_2 \end{array}$$

$$\begin{array}{r} 376_8 \\ + 217_8 \\ \hline 667_8 \\ 1505_8 \end{array}$$

$$\begin{array}{r} A\ B\ C\ 2\ F\ (16) \\ 1\ 5\ 4\ 7\ C\ (16) \\ \hline 2\ 1\ 0\ A\ B \end{array}$$

* RESTO *

a). - NATURAL

b). - PRESTAMO (BORROW)

MINUENDO

- SUSTRAYENDO

DIFERENCIA

$$\begin{array}{r} 712_{10} \\ - 593_{10} \\ \hline 019_{10} \end{array}$$

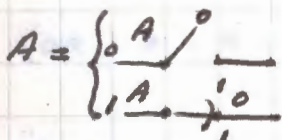
$$\begin{array}{r} 101100_2 \\ - 101101_2 \\ \hline 001111_2 \end{array}$$

$$\begin{array}{r} 101100_2 \\ - 11101_2 \\ \hline 01111_2 \end{array}$$

$$\begin{array}{r} 100110110 \text{ (2)} \\ - 11001111 \text{ (2)} \\ \hline 099808999 \\ \hline 11100111 \end{array} \quad \begin{array}{r} 101100010 \text{ (2)} \\ - 11111111 \text{ (2)} \\ \hline 011100001 \text{ (2)} \end{array} \quad \begin{array}{r} 62806 \text{ (8)} \\ - 7777 \text{ (8)} \\ \hline 50025 \text{ (8)} \end{array} \quad \begin{array}{r} 60021 \text{ (8)} \\ - 7777 \text{ (8)} \\ \hline 50025 \text{ (8)} \end{array}$$

$$\begin{array}{r} 62305 \text{ (8)} \\ - 33156 \text{ (8)} \\ \hline 27127 \text{ (8)} \end{array} \quad \begin{array}{r} 27302 \text{ (8)} \\ - 17402 \text{ (8)} \\ \hline 07700 \text{ (8)} \end{array} \quad \begin{array}{r} A0201 \text{ (16)} \\ - ABDF \text{ (16)} \\ \hline 85622 \text{ (16)} \end{array}$$

* ALGEBRA BOOLEANA (GEORGE BOOLE)



Nº DE COMBINACIONES = 2^N
 $N \Rightarrow$ NUMERO DE VARIABLES

* TABLAS DE VERDAD *

ENTRADA	SALIDA
A	S
0	?
1	?

M	A	B	S
0	0	0	
0	0	1	
3	1	1	

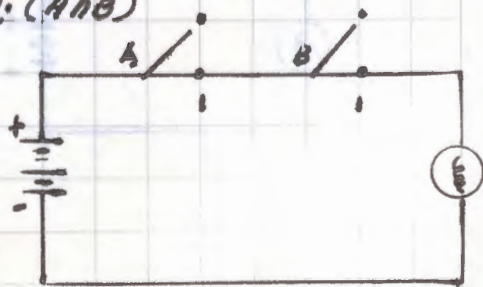
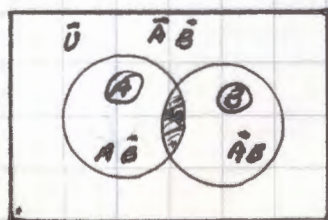
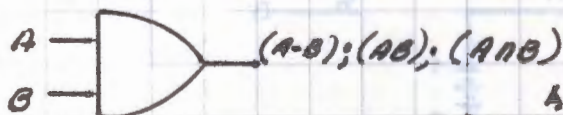
M	A	B	C
0	0	0	0
1	0	0	1
2	0	1	0
3	0	1	1
4	1	0	0
5	1	0	1
6	1	1	0
7	1	1	1

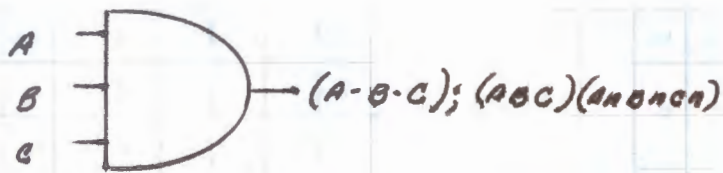
OPERADORES

(AND) * y *
 "CONDICION"

"SIMBOLO"

A	B	A · B
0	0	0
0	1	0
1	0	0
1	1	1





M	A	B	C	(A · B · C) (ABC) (AND AND AND)
0	0	0	0	0
1	0	0	1	0
2	0	1	0	0
3	0	1	1	0
4	1	0	0	0
5	1	0	1	0
6	1	1	0	0
7	1	1	1	1

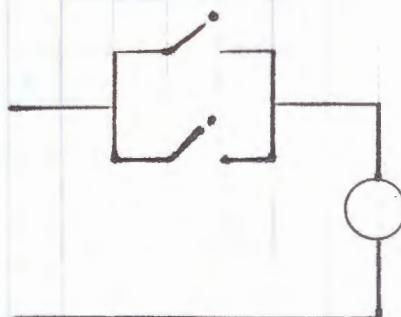
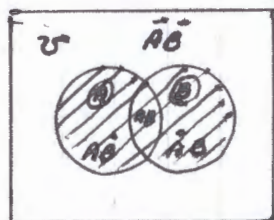
(OR) * 0 *

"ALTERNATIVA"

"SIMBOLO"

"CIRCUITO ELECTRICO EQUIVALENTE"

M	A	B	S
0	0	0	0
1	0	1	1
2	1	0	1
3	1	1	1

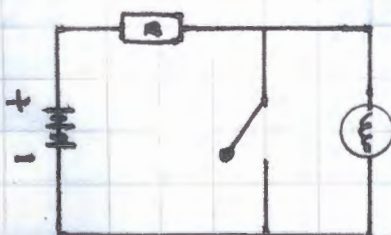
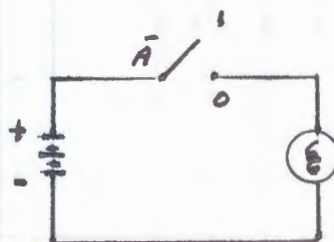


(NOT) * NEGAR *

"SIMBOLO"

"CIRCUITO ELECTRICO EQUIVALENTE"

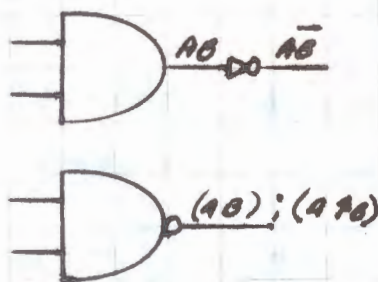
A	Ā
0	1
1	0



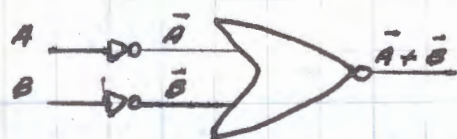
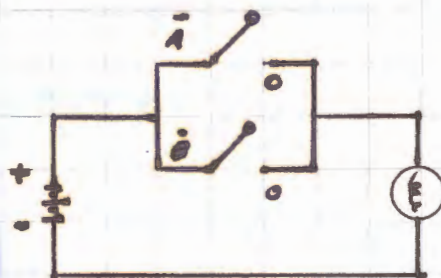
M	A	B	AND	OR
0	0	0	0	0
1	0	1	0	1
2	1	0	0	1
3	1	1	1	1

M	A	NOT
0	0	1
1	1	0

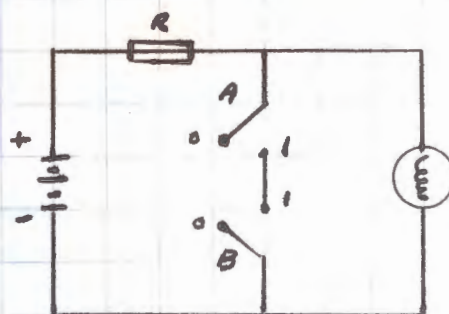
(NAND) * NO-AND * TABLA DE VERDAD * CIRCUITO ELECTRICO EQUIVALENTE



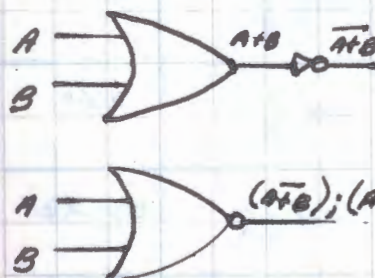
M	A	B	A+B
0	0	0	1
1	0	1	1
2	1	0	1
3	1	1	0



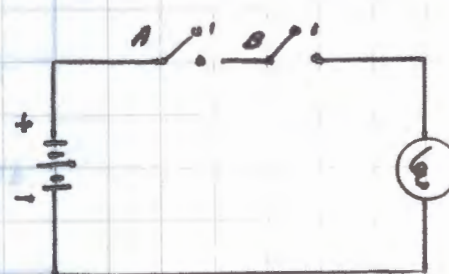
$$\bar{A} + \bar{B} = \overline{A \cdot B}$$



(NOR) "SIMBOLO" * TABLA DE VERDAD * CIRCUITO ELECTRICO EQUIVALENTE *



M	A	B	A+B
0	0	0	1
1	0	1	0
2	1	0	0
3	1	1	0

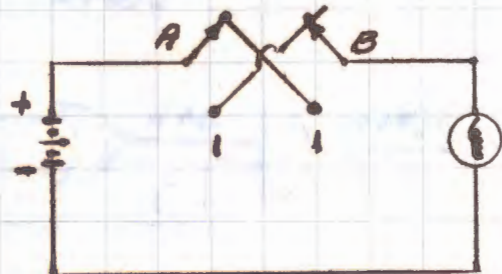


(EX-OR) OR-EXCLUSIVO, EL EX-OR ES VERDADERO CUANDO UN NUMERO IMPAR DE VARIABLES DE ENTRADAS SON VERDADERAS.

"SIMBOLO"



N	A	B	$A \oplus B$
0	0	0	0
1	0	1	1
2	1	0	1
3	1	1	0

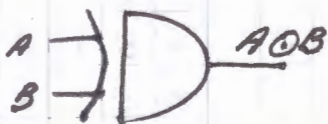


(EX-NOR) : ≠ COINCIDENCIA = AND-COINCIDENCIA ≠

"SIMBOLO"



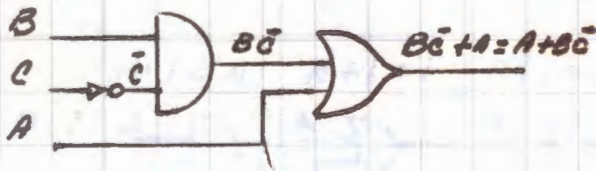
N	A	B	$A \odot B$
0	0	0	1
1	0	1	0
2	1	0	0
3	1	1	1



NOMBRE	SIMBOLO	EXPRESSION	TABLA										
AND		$A \cdot B$ $A \odot B$	<table border="1"> <thead> <tr> <th>AB</th> <th>$A \odot B$</th> </tr> </thead> <tbody> <tr><td>00</td><td>0</td></tr> <tr><td>01</td><td>0</td></tr> <tr><td>10</td><td>0</td></tr> <tr><td>11</td><td>1</td></tr> </tbody> </table>	AB	$A \odot B$	00	0	01	0	10	0	11	1
AB	$A \odot B$												
00	0												
01	0												
10	0												
11	1												
OR		$A + B$	<table border="1"> <thead> <tr> <th>AB</th> <th>$A + B$</th> </tr> </thead> <tbody> <tr><td>00</td><td>0</td></tr> <tr><td>01</td><td>1</td></tr> <tr><td>10</td><td>1</td></tr> <tr><td>11</td><td>1</td></tr> </tbody> </table>	AB	$A + B$	00	0	01	1	10	1	11	1
AB	$A + B$												
00	0												
01	1												
10	1												
11	1												
NAND		$(\bar{A} \cdot \bar{B}) ; (\overline{A \cdot B})$	<table border="1"> <thead> <tr> <th>AB</th> <th>$\overline{A \cdot B}$</th> </tr> </thead> <tbody> <tr><td>00</td><td>1</td></tr> <tr><td>01</td><td>1</td></tr> <tr><td>10</td><td>1</td></tr> <tr><td>11</td><td>0</td></tr> </tbody> </table>	AB	$\overline{A \cdot B}$	00	1	01	1	10	1	11	0
AB	$\overline{A \cdot B}$												
00	1												
01	1												
10	1												
11	0												
NOR		$(\bar{A} + \bar{B}) ; \overline{(A + B)}$	<table border="1"> <thead> <tr> <th>AB</th> <th>$\overline{(A + B)}$</th> </tr> </thead> <tbody> <tr><td>00</td><td>1</td></tr> <tr><td>01</td><td>0</td></tr> <tr><td>10</td><td>0</td></tr> <tr><td>11</td><td>0</td></tr> </tbody> </table>	AB	$\overline{(A + B)}$	00	1	01	0	10	0	11	0
AB	$\overline{(A + B)}$												
00	1												
01	0												
10	0												
11	0												
EX-OR		$A \oplus B$	<table border="1"> <thead> <tr> <th>AB</th> <th>$A \oplus B$</th> </tr> </thead> <tbody> <tr><td>00</td><td>0</td></tr> <tr><td>01</td><td>1</td></tr> <tr><td>10</td><td>1</td></tr> <tr><td>11</td><td>0</td></tr> </tbody> </table>	AB	$A \oplus B$	00	0	01	1	10	1	11	0
AB	$A \oplus B$												
00	0												
01	1												
10	1												
11	0												
EX-NOR COINCIDENCIA		$(\bar{A} \odot \bar{B}) ; (A \odot B)$	<table border="1"> <thead> <tr> <th>AB</th> <th>$A \odot B$</th> </tr> </thead> <tbody> <tr><td>00</td><td>1</td></tr> <tr><td>01</td><td>0</td></tr> <tr><td>10</td><td>0</td></tr> <tr><td>11</td><td>1</td></tr> </tbody> </table>	AB	$A \odot B$	00	1	01	0	10	0	11	1
AB	$A \odot B$												
00	1												
01	0												
10	0												
11	1												
NOT		\bar{A}	<table border="1"> <thead> <tr> <th>A</th> <th>\bar{A}</th> </tr> </thead> <tbody> <tr><td>0</td><td>1</td></tr> <tr><td>1</td><td>0</td></tr> </tbody> </table>	A	\bar{A}	0	1	1	0				
A	\bar{A}												
0	1												
1	0												

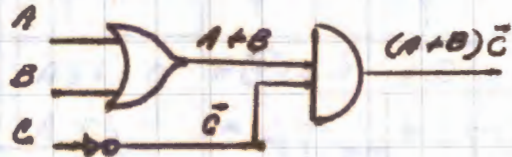
* EXPRESIONES BOOLEANAS *

a). $A + B\bar{C}$



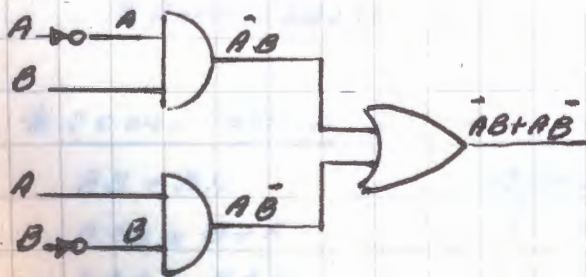
M	A	B	C	$A + B\bar{C}$
0	0	0	0	0
1	0	0	1	0
2	0	1	0	1
3	0	1	1	0
4	1	0	0	1
5	1	0	1	1
6	1	1	0	1
7	1	1	1	1

b). $(A+B)\bar{C}$



M	A	B	C	$(A+B)\bar{C}$
0	0	0	0	0
1	0	0	1	0
2	0	1	0	1
3	0	1	1	0
4	1	0	0	1
5	1	0	1	0
6	1	1	0	1
7	1	1	1	0

a) $\bar{A}B + A\bar{B}$

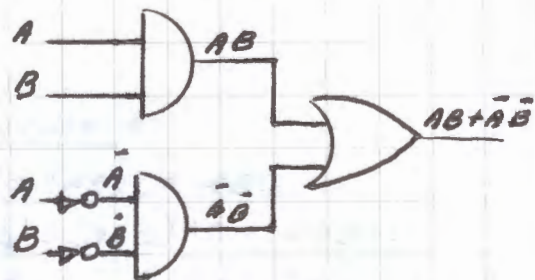


M	A	B	$\bar{A}B + A\bar{B}$
0	0	0	0
1	0	1	1
2	1	0	1
3	1	1	0

* SE COMPORTA COMO UN EX-OR

* $\bar{A}B + A\bar{B} = A \oplus B$ *

b). $AB + \bar{A}\bar{B}$

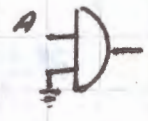


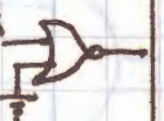
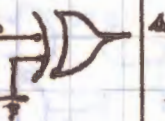
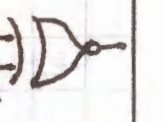
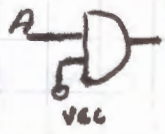
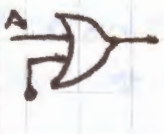
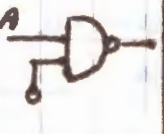
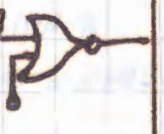
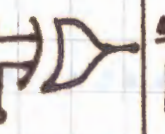

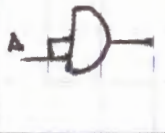
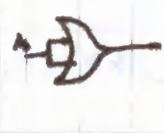
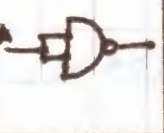
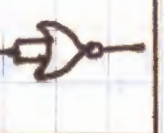
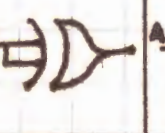
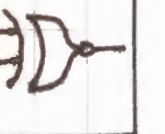
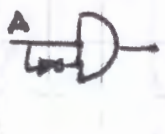

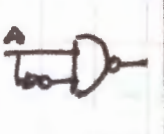
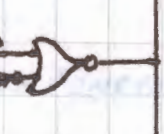
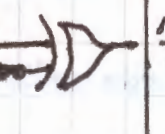
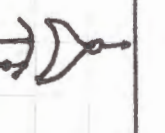


M	A	B	$AB + \bar{A}\bar{B}$
0	0	0	1
1	0	1	0
2	1	0	0
3	1	1	1

SE COMPORTA COMO UN EX-NOR

* $AB + \bar{A}\bar{B} = A \odot B$ *

IDENTIDADES

X Y	AND	OR	NAND	NOR	EX-OR	EX-NOR
0 0	$A \cdot 0 = 0$	$A + 0 = A$	$A \uparrow 0 = 1$	$A \downarrow 0 = \bar{A}$	$A \oplus 0 = A$	$A \odot 0 = \bar{A}$
1 0						
0 1	$A \cdot 1 = A$	$A + 1 = 1$	$A \uparrow 1 = \bar{A}$	$A \downarrow 1 = 0$	$A \oplus 1 = \bar{A}$	$A \odot 1 = A$
1 1						
0 0	$A \cdot A = A$	$A + A = A$	$A \uparrow A = \bar{A}$	$A \downarrow A = \bar{A}$	$A \oplus A = 0$	$A \odot A = 1$
1 1						
0 1	$A \cdot \bar{A} = 0$	$A + \bar{A} = 1$	$A \uparrow \bar{A} = 1$	$A \downarrow \bar{A} = 0$	$A \oplus \bar{A} = 1$	$A \odot \bar{A} = 0$
1 0						

NOT
 $\bar{A} = A$ 

* PROPIEDADES

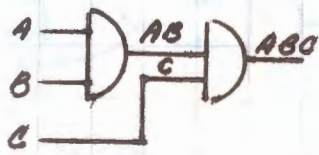
* COMUTATIVA *

$$\begin{aligned}
 AB &= BA \\
 A+B &= B+A \\
 A \uparrow B &= B \uparrow A \\
 A \downarrow B &= B \downarrow A \\
 A \oplus B &= B \oplus A \\
 A \odot B &= B \odot A
 \end{aligned}$$

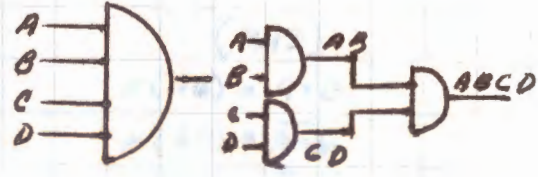
* ASOCIATIVA *

$$\begin{aligned}
 A(BC) &= (AB)C = ABC \\
 A+(B+C) &= (A+B)+C = A+B+C \\
 A \oplus (B \oplus C) &= (A \oplus B) \oplus C = A \oplus B \oplus C \\
 &(\text{NAND}); (\text{NOR}); (\text{EX-NOR}) \\
 A \uparrow (B \uparrow C) &\neq (A \uparrow B) \uparrow C \neq (A \uparrow B) \uparrow C \\
 A \downarrow (B \downarrow C) &\neq (A \downarrow B) \downarrow C \neq (A \downarrow B) \downarrow C \\
 A \odot (B \odot C) &\neq (A \odot B) \odot C \neq A \odot B \odot C
 \end{aligned}$$

* PARA TRES ENTRADAS *



* PARA CUATRO ENTRADAS *



* PROPIEDAD DISTRIBUTIVA *

$$\rightarrow A + BC = (A + B)(A + C) \rightarrow AA + AC + AB + BC = A(A + C) + AB = A(1 + C) + BC$$

$$\rightarrow A(1 + B) + BC \rightarrow A(1) + BC \rightarrow \underline{A + BC} \quad *$$

$$\rightarrow A + \bar{A}C = (A + \bar{A})(A + C) = 1(A + C) \rightarrow \underline{A + C} \quad *$$

* TEOREMA DE MORGAN *

" COMPLEMENTO INVERSO DE UNA FUNCION BOOLEANA "

REGLAS:

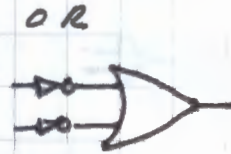
- 1.- SOLO PARA EXPRESIONES NEGADAS
- 2.- REEMPLAZAR TODOS LOS OPERADORES AND POR OR
- 3.- REEMPLAZAR TODOS LOS OPERADORES OR POR AND
- 4.- REEMPLAZAR TODAS LAS VARIABLES POR SU COMPLEMENTO
- 5.-

* (NAND) *

$$F(AB) = A + B$$

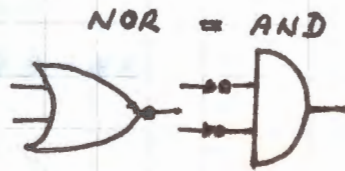
$$F(A\bar{B}) = A \cdot B$$

$$F(\bar{A}B) = \bar{A} + \bar{B}$$

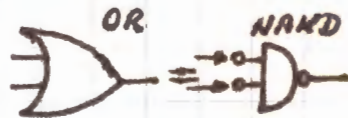


A	B	A + B	A · B
0	0	1	1
0	1	1	1
1	0	1	1
1	1	0	0

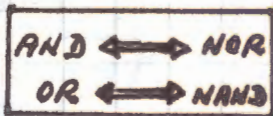
(NOR)
 $F(AB) = A \downarrow B$
 $F(AB) = \overline{A+B}$
 $F(AB) = \overline{A} \cdot \overline{B}$



(OR)
 $F(AB) = A + B$
 $F(AB) = \overline{\overline{A+B}}$
 $F(AB) = \overline{\overline{A} \cdot \overline{B}}$
 $F(AB) = \overline{\overline{A} \cdot \overline{B}}$
 $F(AB) = \overline{\overline{A}} + \overline{\overline{B}}$



(AND)
 $F(AB) = A \cdot B$
 $F(AB) = \overline{\overline{A \cdot B}}$
 $F(AB) = \overline{\overline{A} + \overline{B}}$
 $F(AB) = \overline{\overline{A} + \overline{B}}$
 $F(AB) = \overline{\overline{A}} \downarrow \overline{\overline{B}}$



"TEOREMA DE MORGAN EN EXPRESIONES"

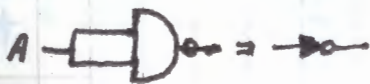
① $F(ABC) = \overline{A} + (\overline{BC}) \rightarrow =$
 $F(ABC) = A \uparrow (\overline{B} \downarrow \overline{C})$
 $F(ABC) = A \uparrow (\overline{B+C})$
 $F(ABC) = \overline{\overline{A} + (\overline{B+C})} *$

② $F(ABCD) = BC + (A \uparrow B \uparrow D)$
 $F(ABCD) = BC + (\overline{A+B+D})$
 $F(ABCD) = \overline{BC} + (\overline{A+B+D})$
 $F(ABCD) = (\overline{B+C}) + (\overline{A+B+D}) *$

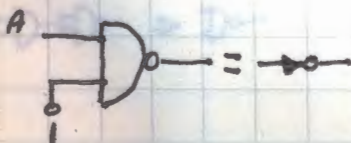
* SUSTITUCIÓN DE EXPRESIONES BOOLEANAS POR :

- ① - NAND'S
- ② - NOR'S

1) a) NOR a PARTIR NAND'S
 $A \uparrow A = \overline{A}$



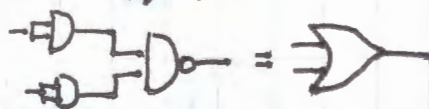
$A \uparrow 1 = A$



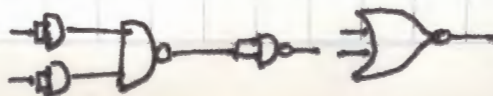
1) b) AND



1) c) OR

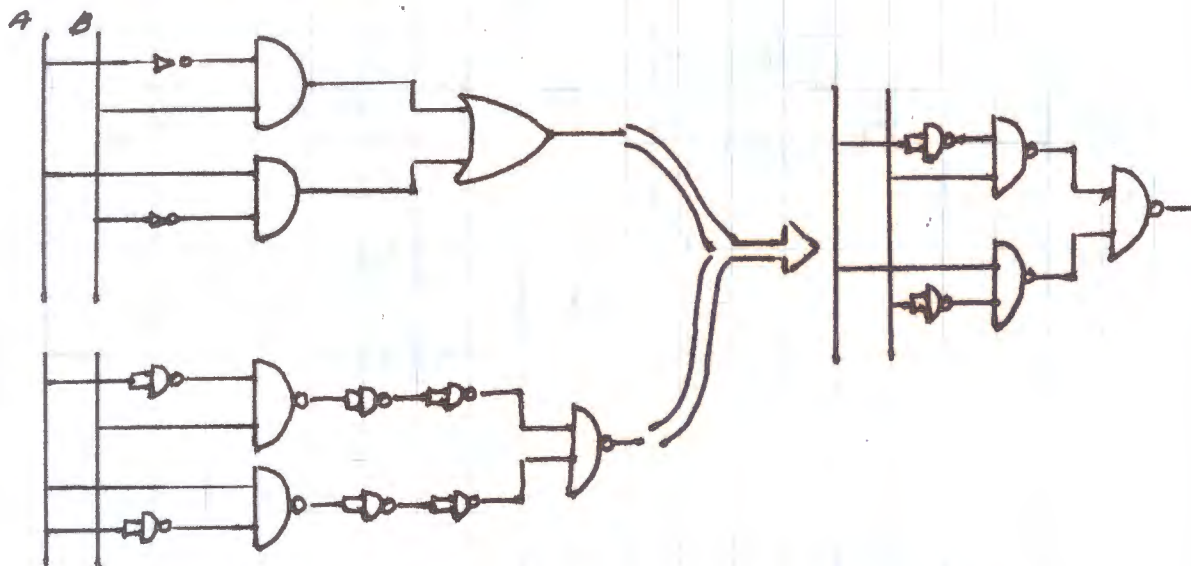


1) d) NOR



1) :- 6) EX OR

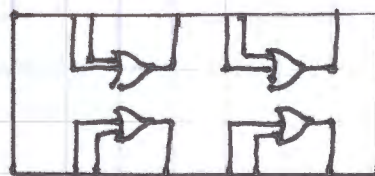
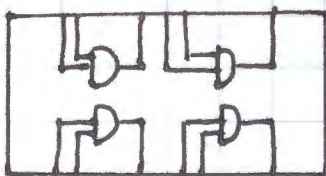
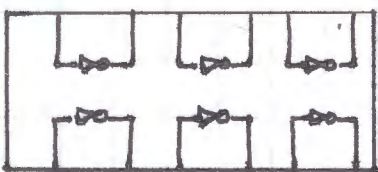
$$A \oplus B = \bar{A}B + A\bar{B}$$



UN CHIP DE (NOT)
SN 7404 2/6 33%

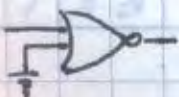
UN CHIP DE (AND)
SN 7408 2/4 50%

UN CHIP DE (OR)
SN 7432 1/4 25%



‡ SUSTITUCION DE COMPONENTES POR (NOR'S)

2a) NOT



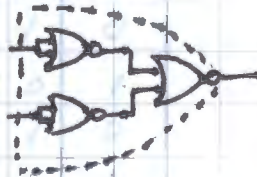
$A \downarrow A$



2b) OR



2c) AND



2d) NAND

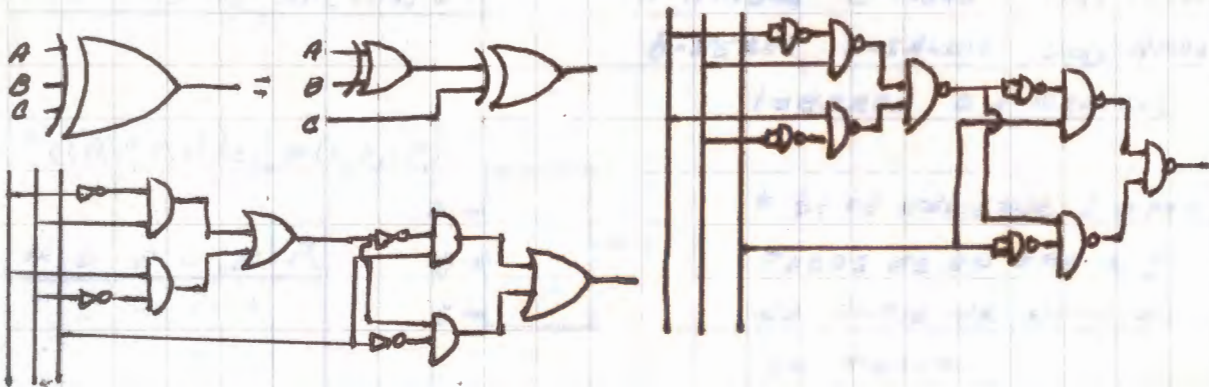


2e) EX-OR

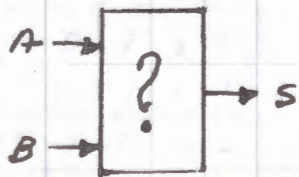
$$A \oplus B \oplus C$$



REEMPLAZAR UN (EX-OR) DE TRES ENTRADAS POR COM-
PUERTAS NAND'S DE DOS ENTRADAS



* MINITERMINO *



A	B	S	S ₁	S ₂	S ₃
0	0	0	0	0	1
0	1	1	0	0	0
1	0	0	0	1	0
1	1	0	1	0	0

A	B	MINI
0	0	$\bar{A}\bar{B}$
0	1	$\bar{A}B$
1	0	$A\bar{B}$
1	1	AB

A	B	$A \oplus B$
0	0	0
0	1	1
1	0	1
1	1	0

$$F(A,B) = \bar{A}\bar{B} + \bar{A}B + A\bar{B}$$

M	A	B	C	F ₂
0	0	0	0	0
1	0	0	1	1
2	0	1	0	0
3	0	1	1	0
4	1	0	0	1
5	1	0	1	0
6	1	1	0	1
7	1	1	1	0

$$F_2(ABC) = \bar{A}\bar{B}C + \bar{A}B\bar{C} + A\bar{B}\bar{C}$$

FORMA CANONICA

$$F_1(ABC) = M_1 + M_4 + M_6$$

$$F_1(ABC) = \sum M(1, 4, 6) *$$

DETECTOR DE ERRORES DE UN SEMAFORO

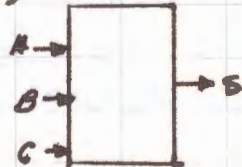
A-AMBAR C-ROJO 1 ⇒ ENCENDIDO

B-VERDE S-SALIDA 0 ⇒ APAGADO

1 = ERROR 0 = NORMAL

$$F_2(D, A, C, B) = \sum M(0, 3, 15)$$

M	D	A	C	B	F ₂
0	0	0	0	0	1
1	0	0	0	1	0
2	0	0	1	0	0
3	0	0	1	1	1
4	0	1	0	0	0
5	0	1	0	1	0
6	0	1	1	0	0
7	0	1	1	1	0
8	1	0	0	0	0
9	1	0	0	1	0
10	1	0	1	0	0
11	1	0	1	1	0
12	1	1	0	0	0
13	1	1	0	1	0
14	1	1	1	0	0
15	1	1	1	1	1



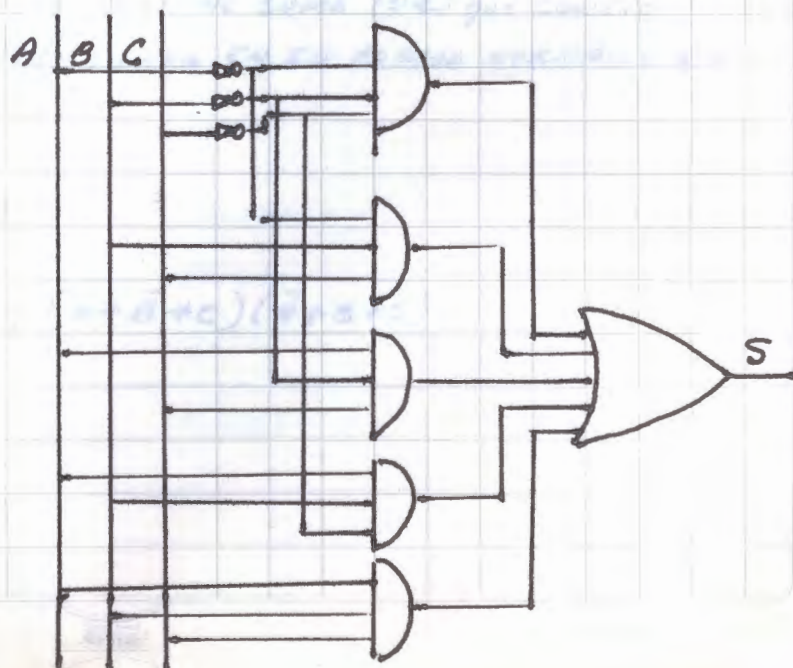
* SI SE ENCIENDE 2 O MAS
FOCOS ES UN ERROR Y SI
NO ENCIENDE NINGUNO ES
UN ERROR

M	A	B	C	S
0	0	0	0	1
1	0	0	1	0
2	0	1	0	0
3	0	1	1	1
4	1	0	0	0
5	1	0	1	1
6	1	1	0	1
7	1	1	1	1

* MINITERMINO ES AQUEL
DONDE LA SALIDA VALE (1)

$$F_1(ABC) = \bar{A}\bar{B}\bar{C} + \bar{A}B\bar{C} + A\bar{B}\bar{C} + A\bar{B}C + ABC$$

$$F_2(ABC) = \sum M(0, 3, 5, 6, 7)$$



M	A	B	C	S	\bar{S}
0	0	0	0	1	0
1	0	0	1	0	1
2	0	1	0	0	1
3	0	1	1	1	0
4	1	0	0	0	1
5	1	0	1	1	0
6	1	1	0	1	0
7	1	1	1	0	1

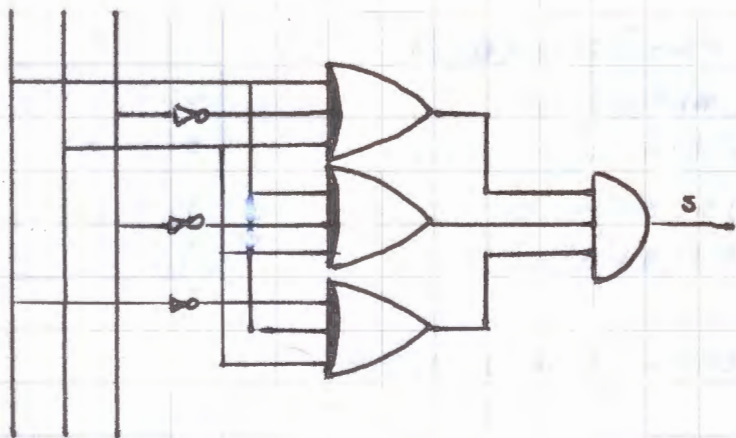
$$\bar{S}(ABC) = \sum m(1, 2, 3)$$

$$\bar{S}(ABC) = \bar{A}\bar{B}C + \bar{A}B\bar{C} + A\bar{B}\bar{C}$$

$$\bar{S}(ABC) = (\bar{A}\bar{B}C) + (\bar{A}B\bar{C}) + (A\bar{B}\bar{C})$$

$$\underline{\underline{S(ABC) = (A+B+\bar{C}) \cdot (A+\bar{B}+C) \cdot (\bar{A}+B+C) \quad *}}$$

\bar{F} COMO ALTERNATIVA "MAXITERMINOS" OR/AND



"MAXITERMINO" \Rightarrow ES UN TERMINO SUMA (OR) QUE CONTIENE TODAS LAS VARIABLES DE LA FUNCION YA SEA EN SU FORMA NORMAL O EN SU FORMA IMPLEMENTADA.

$$F_S(ABC) = \sum n(1, 2, 3)$$

$$F_S(ABC) = (A+B+\bar{C})(A+\bar{B}+C)(\bar{A}+B+C)$$

w/i	A	X	Y	F ₁	F ₂
0	0	0	0	0	0
1	0	0	1	1	1
2	0	1	0	1	0
3	0	1	1	0	0
4	1	0	0	0	0
5	1	0	1	1	1
6	1	1	0	1	0
7	1	1	1	0	0

$$F(X,Y) = \Sigma (1, 2, 5, 6)$$

$$F(X,Y) = \bar{A}\bar{X}Y + \bar{A}X\bar{Y} + A\bar{X}Y + AX\bar{Y}$$

$$F(X,Y) = \Sigma \pi (1, 3)$$

$$F_1 \left\{ \begin{array}{l} F \in m; \text{SOD} \\ F \in \pi; \text{POS} \end{array} \right. \quad F_2 \left\{ ? \right.$$

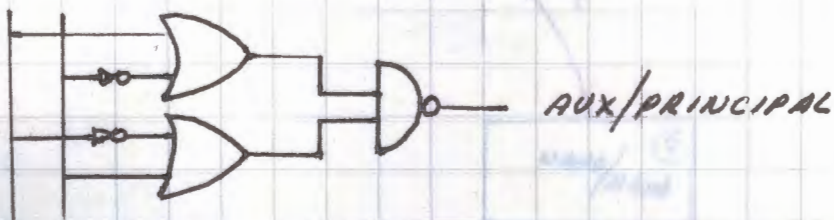
M	A	B	F
0	0	0	0
1	0	1	0
2	1	0	1
3	1	1	1

$$\begin{aligned} F(AB) &= A\bar{B} + AB \quad \text{AND/OR} \\ &= \overline{(A\bar{B})} \uparrow \overline{(AB)} \quad \text{NAND/AND} \\ &= (\bar{A} + B) \uparrow (\bar{A} + \bar{B}) \quad \text{OR/NAND} \\ &= (\bar{A} \downarrow B) + (\bar{A} \downarrow \bar{B}) \quad \text{NOR/OR} \\ &= (\bar{A} + B)(\bar{A} + \bar{B}) \quad \text{OR/AND} \end{aligned}$$

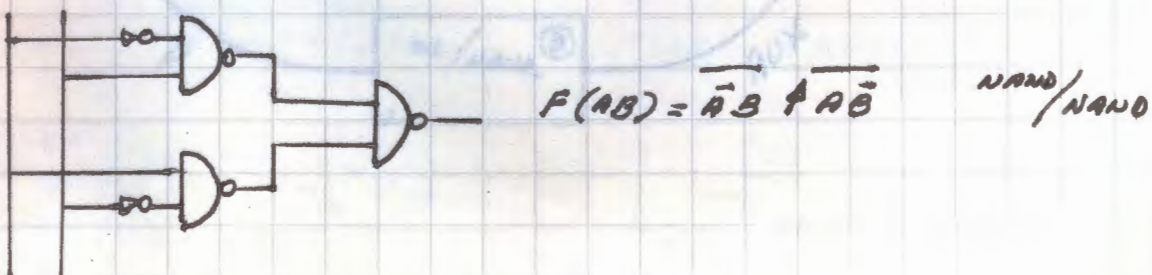
* LAS OCHO FORMULAS STANDARD *

M	A	B	F
0	0	0	0
1	0	1	1
2	1	0	1
3	1	1	0

$$F(AB) = \bar{A}B + A\bar{B} \quad \text{AND/OR}$$

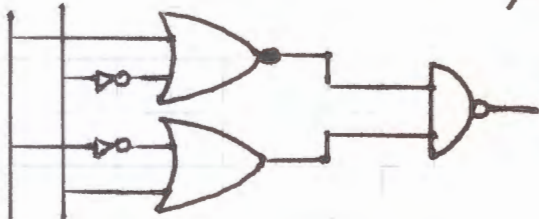


PARA OBTENER LA SEGUNDA STANDARD SE APLICARA EL TEOREMA DE MORGAN A LA COMPUERTA PRINCIPAL DE LA PRIMERA FORMA STANDARD

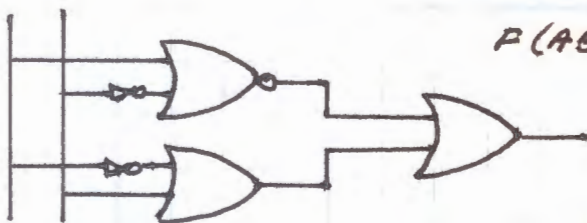


LA TERCER FORMA STANDARD SE OBTIENE A PARTIR DE LA SEGUNDA, APLICANDO EL TEOREMA DE MORGAN A LOS AUXILIARES (NAND'S)

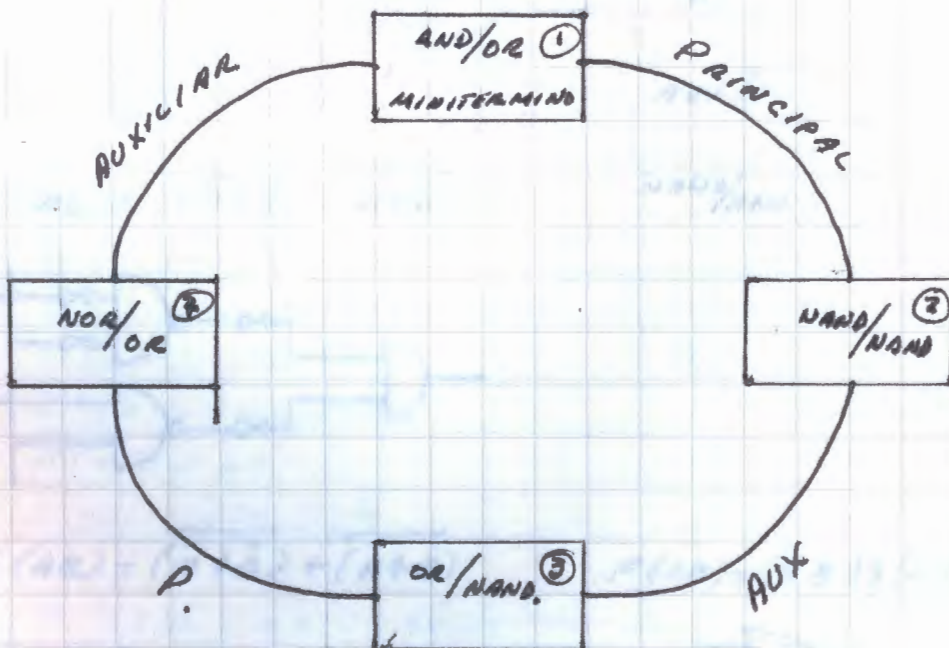
$$F(AB) = \overline{(A+B)} + \overline{(A+B)} \quad \text{NOR/NAND}$$

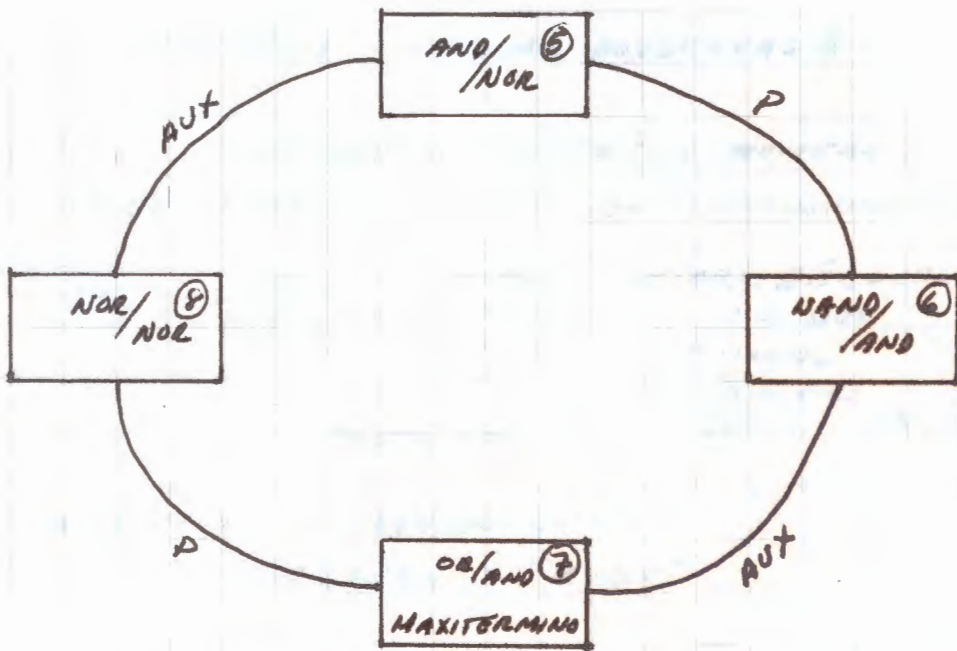


LA CUARTA FORMA SE OBTIENE A PARTIR DE LA TERCERA APLICANDO EL TEOREMA DE MORGAN EN LA PRINCIPAL (NAND)



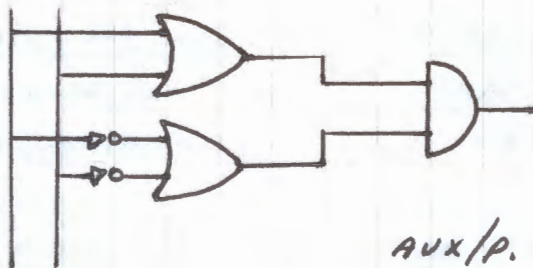
$$F(AB) = \overline{(A+B)} + \overline{(A+B)} \quad \text{NOR/OR}$$





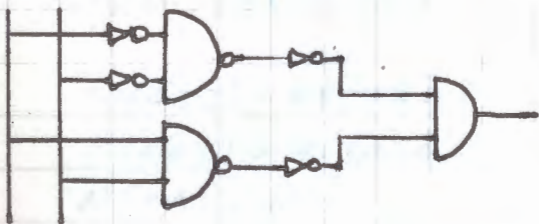
$$F(A,B) = (A+B)(\bar{A}+\bar{B}) \text{ OR/AND.}$$

M	A	B	
0	0	0	0
1	0	1	1
2	1	0	1
3	1	1	0



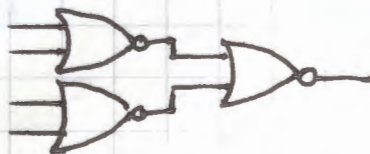
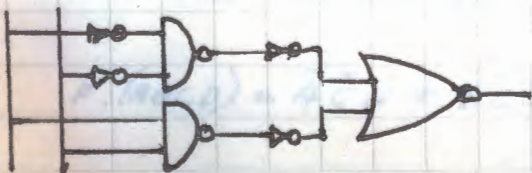
$$F(A,B) = \overline{(A+B)} \cdot \overline{(\bar{A}+\bar{B})}.$$

NAND/AND



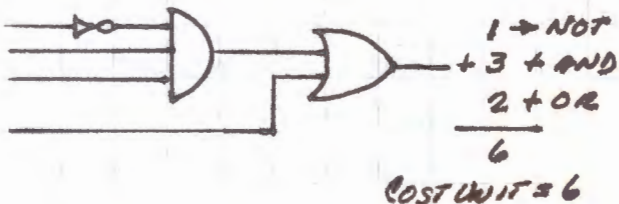
$$F(A,B) = \overline{(\bar{A}+\bar{B})} + \overline{(A+B)}$$

$$F(A,B) = (\bar{A}\bar{B}) \downarrow (AB) (\bar{A}+\bar{B}) \downarrow (A+B)$$



* MINIMIZACION DE FUNCIONES BOOLEANAS *

COST UNIT. (COSTO UNITARIO) "C.U." = ES EL NUMERO DE ENTRADAS O SUMA DE ENTRADAS DE TODOS LOS COMPONENTES DEL CIRCUITO



$$F(AB) = A\bar{B}C + \bar{A}BC + ABC$$

2 → NOT
+ 9 → AND

14
COST. UNIT. 14

- a) MANIPULACION ALGEBRAICA
b) MAPAS DE KARNAUGH O "KMAP"

- a) 1.- FACTORIZACION
2.- DOPICADO UN TERMINO YA EXISTENTE
3.- MULTIPLICANDO POR UN FACTOR DE LA FORMA $(A + \bar{A}) = 1$
4.- PROPIEDAD DISTRIBUTIVA.

2.- $F(AB) = \bar{A}\bar{B} + \bar{A}B + A\bar{B} + AB$
 $F(AB) = \bar{A} + B$ *

$F(ABC) = \bar{A}\bar{B}C + \bar{A}B\bar{C} + \bar{A}BC + A\bar{B}C + ABC$
 $F(ABC) = \bar{A}\bar{B} + \bar{A}B + BC$
 $F(ABC) = \bar{A} + BC$ *

1.- $F(AB) = \bar{A}\bar{B} + \bar{A}B$
 $F(AB) = \bar{A}(B + \bar{B})$
 $F(AB) = \bar{A}$
COST. UNIT. = 1 *

- 3.- MULTIPLICANDO POR UN FACTOR DE LA FORMA $(A + \bar{A})$

$F(ABC) = ABC + A\bar{B}C$
 $F(ABC) = AC(B + \bar{B})$
 $F(ABC) = AC$
COST. UNIT. 2 *

$F_c(xyz) = xz + \bar{x}y + yz$ CN=10
 $F_c(xyz) = xz + \bar{x}y + yz(x + \bar{x})$
 $= xz + \bar{x}y + xyz + \bar{x}yz$
 $= xz(1+y) + \bar{x}y(1+z)$
 $= xz + \bar{x}y$ * C.U. = 7

$F(ABCD) = \bar{A}\bar{B}\bar{C}\bar{D} + \bar{A}\bar{B}\bar{C}D + \bar{A}\bar{B}C\bar{D} + \bar{A}\bar{B}CD$

$F(ABCD) = \bar{A}\bar{C}D + \bar{B}\bar{C}\bar{D}$

PROPIEDAD DISTRIBUTIVA

$$a+bc = (a+b)(a+c)$$

$$a+\bar{a}c = (a+a')(a+c)$$

$$a+\bar{a}c = a+c$$

$$F(ABC) = \bar{A}\bar{B}\bar{C} + \bar{A}B\bar{C} + \bar{A}BC + A\bar{B}\bar{C} + ABC$$

$$F(ABC) = \bar{A}\bar{B} + \bar{A}B + BC$$

$$F(ABC) = \underline{\bar{A} + BC} *$$

$$A + \bar{A} = 1$$

$$F(ABC) = \bar{A}\bar{B}\bar{C} + \bar{A}\bar{B}C + \bar{A}B\bar{C} + \bar{A}BC$$

$$AB + \bar{A}\bar{B} \neq 1$$

$$ABC + \bar{A}\bar{B}\bar{C} \neq 1$$

} NO SON IGUALES A 1

$$F(ABC) = \bar{A}\bar{B} + \bar{A}C + AB$$

$$F(ABC) = \bar{B} + \bar{B}C + A$$

$$F(ABC) = (\bar{B} + C)(\bar{B} + A)$$

$$F(ABC) = \underline{B + C + A} * C.U.: 5$$

$$\bar{A}\bar{B} + \bar{A}B + \bar{A}B + AB = 1$$

$$\bar{A} + A = 1$$

$$F_1(ABCD) = \sum_n (0, 4, 5, 7, 8, 12, 13, 15)$$

$$F_2(xy) = xy(x+y)$$

$$F_3(ABC) = \bar{A}\bar{B}\bar{C} + \bar{A}\bar{B}C + A\bar{B}\bar{C} + ABC$$

$$F_4(ABCDE) = (A+B)(A+C)(A+D)(A+E)$$

$$F_1(ABCD) = \bar{A}\bar{B}\bar{C}\bar{D} + \bar{A}\bar{B}\bar{C}D + \bar{A}\bar{B}C\bar{D} + \bar{A}\bar{B}CD + \bar{A}B\bar{C}\bar{D} + \bar{A}B\bar{C}D + \bar{A}BC\bar{D} + \bar{A}BCD$$

$$= \bar{A}\bar{C}\bar{D} + \bar{A}BD + A\bar{C}\bar{D} + ABD$$

$$= \underline{\bar{C}\bar{D} + BD} * C.U. = 7$$

$$F_2(xy) = xy(x+y)$$

$$= xxy + xyy$$

$$= xy + xy$$

$$= xy(1+1)$$

$$= \underline{xy} *$$

$$F_3(ABC) = \bar{A}\bar{B}\bar{C} + \bar{A}\bar{B}C + \bar{A}B\bar{C} + ABC$$

$$= \underline{\bar{A}\bar{B} + AB} *$$

$$\begin{aligned}
 F_4 &= (ABCDE)(A+B)(A+C)(A+D)(A+E) \\
 F_4 &= (ABCDE) = (A+AC+AB+BC)(A+B)(A+E) \\
 &= [A(1+C+B)+BC](A+B)(A+E) \\
 &= (A+BC)(A+B)(A+E) \\
 &= (A+AD+ABC+BCD)(A+E) \\
 &= [A(1+D+BC)+BCD](A+E) \\
 &= (A+BCD)(A+E) \\
 &= \underline{A+BCDE} \quad *
 \end{aligned}$$

EJEMPLO:

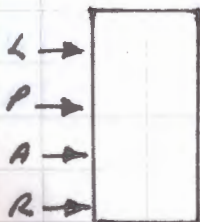
REPRESENTAR EN UNA FUNCIÓN BOOLEANA UN SISTEMA DE CONTROL DE CALIDAD EN UNA FABRICA.

A LAS PIEZAS QUE SE PRODUCEN SE VERIFICAN SUS CARACTERÍSTICAS DE MODO QUE CUANDO LA FUNCIÓN ES VERDADERA ESTO INDICA QUE LA PIEZA ES DEFECTUOSA.

LAS CARACTERÍSTICAS A COMPROBAR SON: LONGITUD, PESO, ACABADO Y RESISTENCIA.

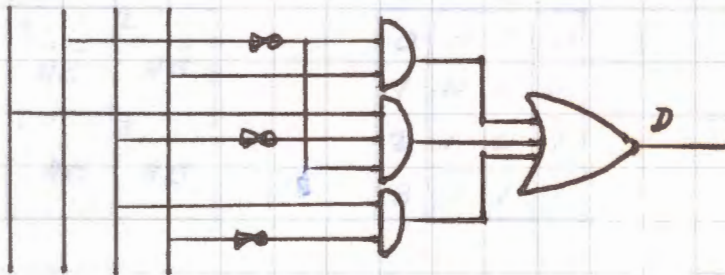
SE DICE QUE UNA PIEZA ES DEFECTUOSA CUANDO CUMPLA CON CUALQUERA DE LAS SIGUIENTES CONDICIONES.

- a). - LA PIEZA TIENE MAL ACABADO CON BUENA RESISTENCIA.
- b). - LA LONGITUD ES ADECUADA PERO EL PESO Y EL ACABADO SON MALOS.
- c). - LA LONGITUD ES ADECUADA, EL ACABADO Y LA RESISTENCIA SON MALOS Y EL PESO ADECUADO.
- d). - LONGITUD, ACABADO Y PESO ADECUADO, PERO MALA RESISTENCIA
- e). - PESO ADECUADO, PERO LONGITUD Y RESISTENCIA MALAS.



	NO	SI	
LONGITUD (L)	0	1	} ADECUADA
PESO (P)	0	1	
ACABADO (A)	0	1	
RESISTENCIA (R)	0	1	
DEFECTUOSOS (D)	0		

$$\begin{aligned}
 F(LPAR) &= \bar{A}R + L\bar{P}\bar{A} + L\bar{A}R\bar{P} + L\bar{A}P\bar{R} + P\bar{L}\bar{A} \\
 &= \bar{A}R + L\bar{P}\bar{A} + L\bar{R}P + P\bar{L}\bar{A} \\
 &= \bar{A}R + L\bar{P}\bar{A} + P\bar{R} \quad *
 \end{aligned}$$



MINITERMINOS

M	L	A	P	R	D
0	0	0	0	0	1
1	0	0	0	1	1
2	0	0	1	0	1
3	0	0	1	1	1
4	0	1	0	0	0
5	0	1	0	1	0
6	0	1	1	0	1
7	0	1	1	1	0
8	1	0	0	0	1
9	1	0	0	1	1
10	1	0	1	0	1
11	1	0	1	1	1
12	1	1	0	0	0
13	1	1	0	1	0
14	1	1	1	0	1
15	1	1	1	1	0

$$\begin{aligned}
 F(LAPR) &= \bar{L}\bar{A}\bar{P}\bar{R} + \bar{L}\bar{A}\bar{P}R + \bar{L}\bar{A}P\bar{R} + \bar{L}\bar{A}PR + \\
 &\quad \underline{L\bar{A}\bar{P}\bar{R}} + \underline{L\bar{A}P\bar{R}} + \underline{L\bar{A}PR} + \underline{L\bar{A}P\bar{R}}
 \end{aligned}$$

$$F(LAPR) = \bar{L}\bar{A}\bar{P}\bar{R} + \bar{L}\bar{A}\bar{P}R + \underline{\bar{L}\bar{A}\bar{P}} + \underline{APR} + \underline{\bar{L}\bar{A}P}$$

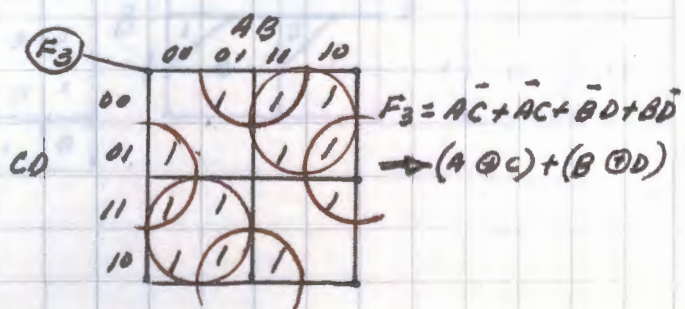
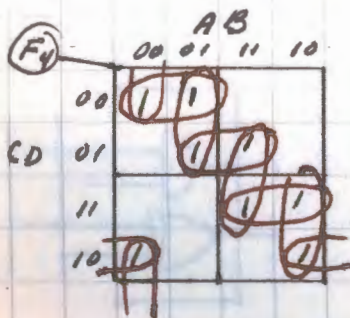
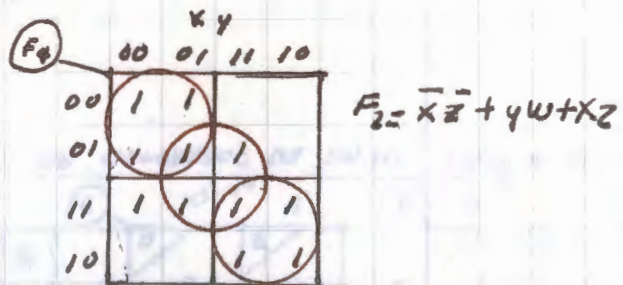
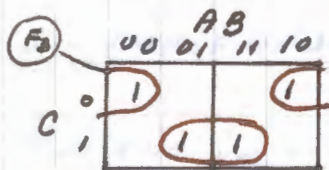
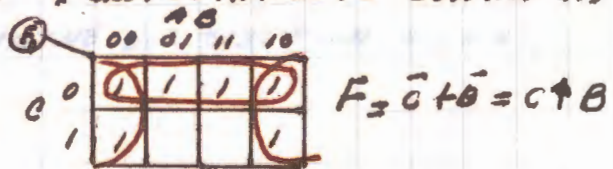
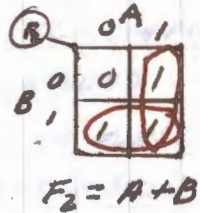
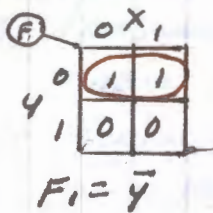
$$F(LAPR) = \bar{L}\bar{A} + \bar{L}\bar{A} + AP\bar{R}$$

$$F(LAPR) = \underline{\bar{L}\bar{A}} + \underline{APR} \quad *$$

REGLAS PARA EL USO DEL MAPA DE KARNAUGH

- 1.- FORMAR LA MENOR CANTIDAD DE GRUPOS
- 2.- CADA GRUPO CON LA MAYOR CANTIDAD DE UNOS (1)
- 3.- EL NUMERO DE UNOS QUE FORMAN UN GRUPO DEBE SER UNA CANTIDAD IGUAL O UNA POTENCIA ENTERA DE DOS (1, 2, 4, 8, 16 ETC.)

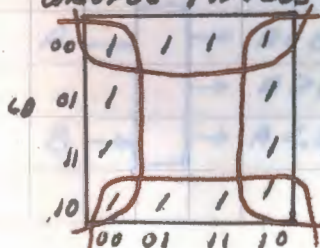
* GRUPOS TÍPICOS DE DOS (2) ≠ GRUPOS TÍPICOS DE CUATRO (4)



$F_4 = \bar{A}\bar{C}\bar{D} + \bar{B}\bar{C}\bar{D} + A\bar{C}\bar{D} + \bar{B}\bar{C}\bar{D}$

$F_4 = \bar{A}\bar{B}\bar{D} + \bar{A}\bar{B}\bar{C} + A\bar{B}\bar{D} + \bar{A}\bar{B}\bar{C}$

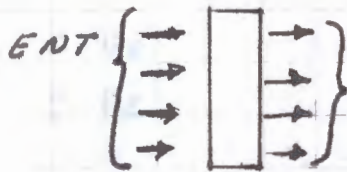
GRUPOS TÍPICOS DE OCHO



GRUPOS NO PERMITIDOS



* SISTEMA COMBINACIONAL *

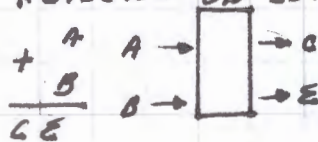


DEFINICIÓN: EN UN SISTEMA COMBINACIONAL LAS SALIDAS SOLO DEPENDEN DE LAS COMBINACIONES DE ENTRADA

METODOLOGIA DEL DISEÑO COMBINACIONAL

- 1.- ESPECIFICAR EL SISTEMA
- 2.- DEFINIR ENTRADAS Y SALIDAS
- 3.- TRASLADAR EL COMPORTAMIENTO DEL SISTEMA A UNA TABLA DE VERDAD
- 4.- MINIMIZAR LA FUNCIÓN
 - a.)- MANIPULACION ALGEBRAICA
 - b.)- MAPA DE KARNAUGH
- 5.- IMPLEMENTAR

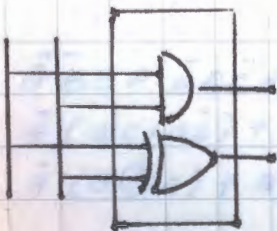
* DISEÑO UN SUMADOR BINARIO DE NUMEROS DE UN BIT CADA NUMERO



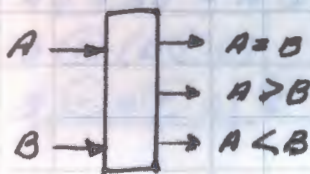
M	A	B	C	E
0	0	0	0	0
1	0	1	0	1
2	1	0	0	1
3	1	1	1	0

		A	
		0	1
C	0	0 / 0	2 / 0
	1	1 / 0	3 / 1

		A	
		0	1
E	0	0 / 0	2 / 1
	1	1 / 1	3 / 0

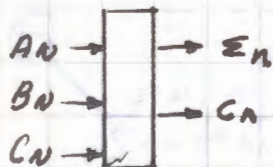
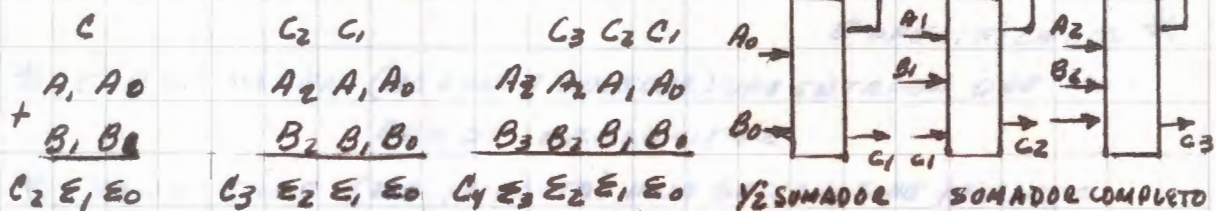


* DISEÑO UN COMPARADOR BINARIO DE LOS NUMEROS DE UN BIT CADA NUMERO A, B; = A=B; A>B; A<B



M	A	B	A=B	A>B	A<B
1	0	0	1	0	0
2	0	1	0	0	1
3	1	0	0	1	0
4	1	1	1	0	0

* DISEÑO POR MÓDULOS *



SUMADOR ENACIMO

$F_{CN} (A_N B_N C_N) = A_N B_N + A_N C_N + B_N C_N$ *

M	A_N	B_N	C_N	C_{N+1}	E_N
0	0	0	0	0	0
1	0	0	1	0	1
2	0	1	0	0	1
3	0	1	1	1	0
4	1	0	0	0	1
5	1	0	1	1	0
6	1	1	0	1	0
7	1	1	1	1	1

$A_N B_N$

C_{N+1}	00	01	11	10
0	0/0	2/0	6/1	4/0
1	1/1	3/0	7/1	5/0

$A_N B_N$

C_N	00	01	11	10
0	0/0	2/1	6/0	4/1
1	1/1	3/0	7/1	5/0

$\bar{A}\bar{B}C + \bar{A}B\bar{C} + A\bar{B}C + A\bar{B}\bar{C}$
 $\bar{A}(\bar{B}C + B\bar{C}) + A(\bar{B}C + B\bar{C})$
 $\bar{A}(B \oplus C) + A(\bar{B} \oplus \bar{C})$

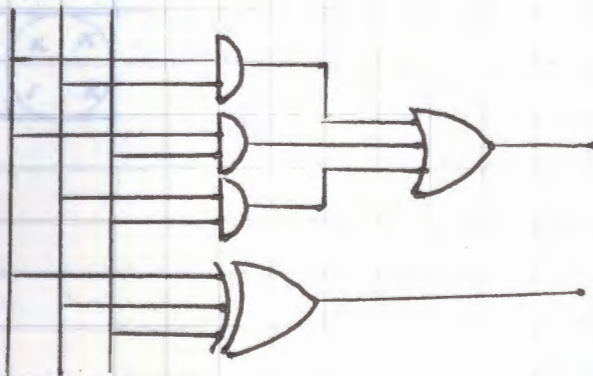
$B \oplus C = Y$

$B \oplus C = \bar{Y}$

$\bar{A}Y + A\bar{Y} = A \oplus Y$

$A \oplus (B \oplus C)$

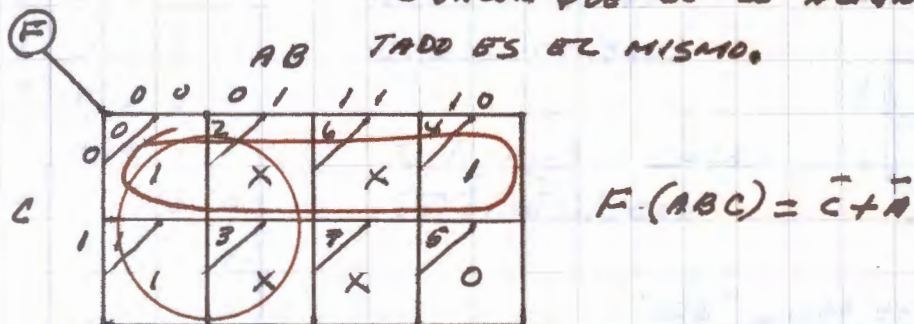
$A \oplus B \oplus C$ *



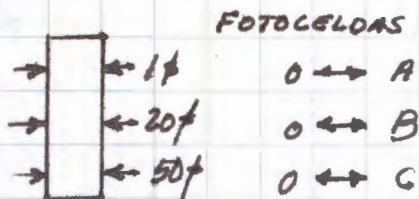
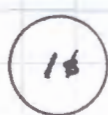
* SISTEMA COMBINACIONAL QUE NO ESTAN COMPLETAMENTE ESPECIFICADOS *

* CAN IT HAPPEN (NO PUEDE SUCCEDER) UNA ENTRADA QUE NO PUEDE PRESENTAR

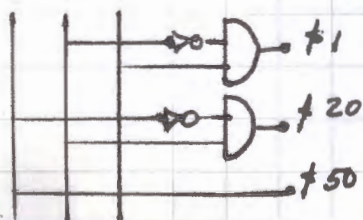
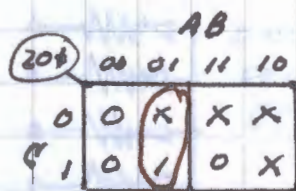
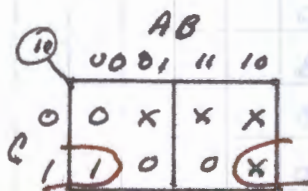
* DON'T CARE (NO IMPORTA) UNA SALIDA QUE IMPORTA EL VALOR QUE SE LE ASIGNA, EL RESULTADO



* DELACTOR DE MONEDAS *



M	A	B	C	1¢	20¢	50¢
0	0	0	0	0	0	0
1	0	0	1	1	0	0
2	0	1	0	X	X	X
3	0	1	1	0	1	0
4	1	0	0	X	X	X
5	1	0	1	X	X	X
6	1	1	0	X	X	X
7	1	1	1	0	0	1



* DECODIFICADOR DE BCD O SIETE SEGMENTOS

BCD = DECIMAL CODIFICADO EN BINARIO

DECIMAL	BCD			
M	B	C	D	
0	0	0	0	0
1	0	0	0	1
2	0	0	1	0
3	0	0	1	1
4	0	1	0	0
5	0	1	0	1
6	0	1	1	0
7	0	1	1	1
8	1	0	0	0
9	1	0	0	1

45(10) = N(BCD)

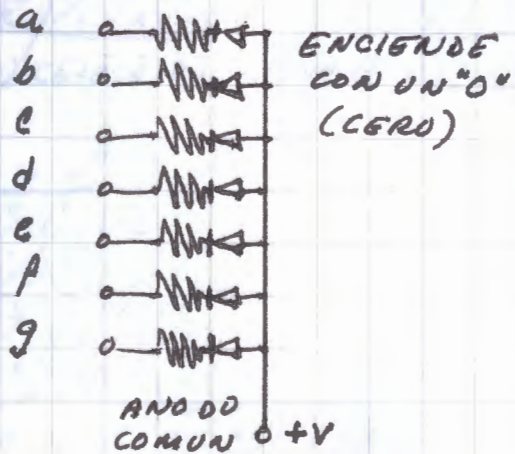
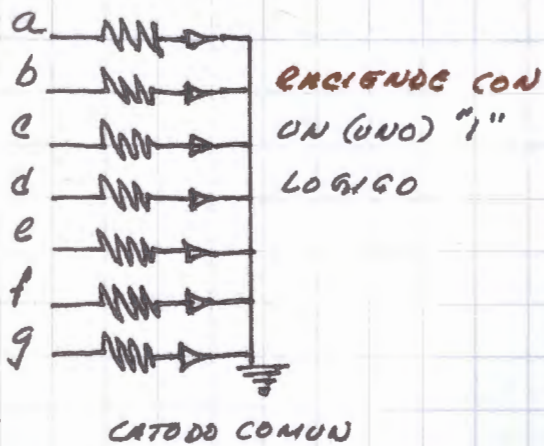
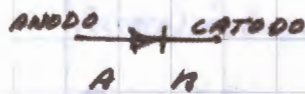
01000101(BCD)

9 7 3 (10) = N(BCD)
 1001011001(BCD)

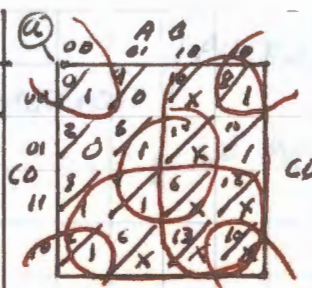
DISPLAY



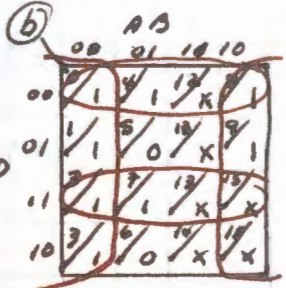
"LED" DIODO EMISOR DE LUZ



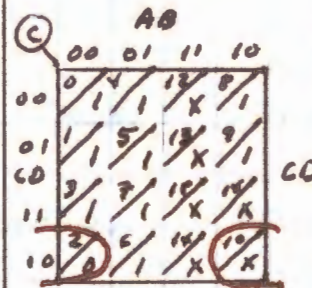
M	A	B	C	D	a	b	c	d	e	f	g
0	0	0	0	0	1	1	1	1	1	1	0
1	0	0	0	1	0	1	1	0	0	0	0
2	0	0	1	0	1	1	0	1	1	0	1
3	0	0	1	1	1	1	1	1	0	0	1
4	0	1	0	0	0	1	1	0	0	1	1
5	0	1	0	1	1	0	1	1	0	1	1
6	0	1	1	0	X	0	1	1	1	1	1
7	0	1	1	1	1	1	1	0	0	X	0
8	1	0	0	0	1	1	1	1	1	1	1
9	1	0	0	1	1	1	1	X	0	1	1
10	1	0	1	0		X	X	X	X	X	X
11	1	0	1	1		X	X	X	X	X	X
12	1	1	0	0		X	X	X	X	X	X
13	1	1	0	1		X	X	X	X	X	X
14	1	1	1	0		X	X	X	X	X	X
15	1	1	1	1		X	X	X	X	X	X



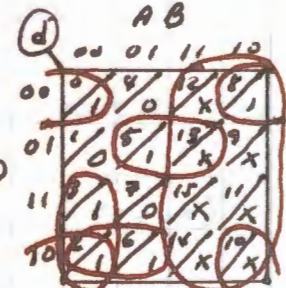
$F_a(ABCD) = A + C + B + D$



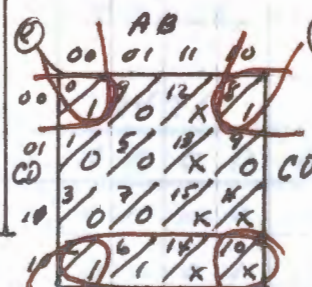
$F_b(ABCD) = B + C + D$



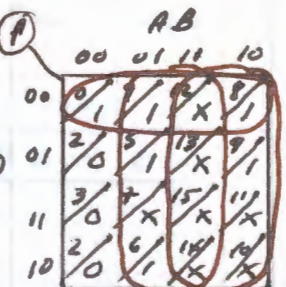
$F_c(ABCD) = B + C + D$



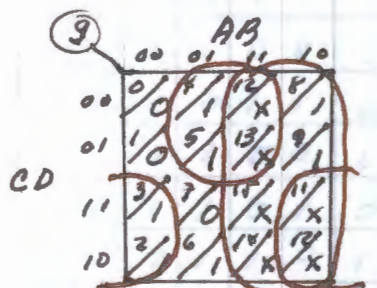
$F_d(ABCD) = A + C + B + D$



$F_e(ABCD) = C + B + D$



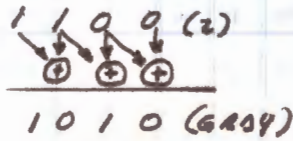
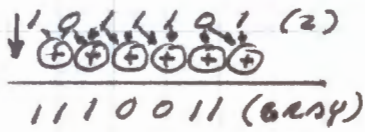
$F_f(ABCD) = A + B + C$



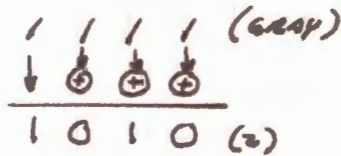
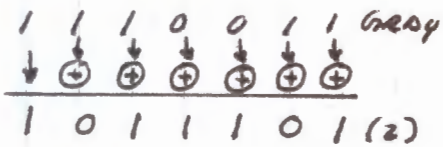
$F_g(ABCD) = A + B + C + D$

COMBINACION DE BINARIO A GRAY

$N(2) \rightarrow N(\text{GRAY})$



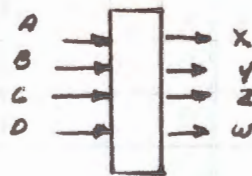
CONVERSION DE GRAY A BINARIO



CONVERTIDOR DE CODIGO

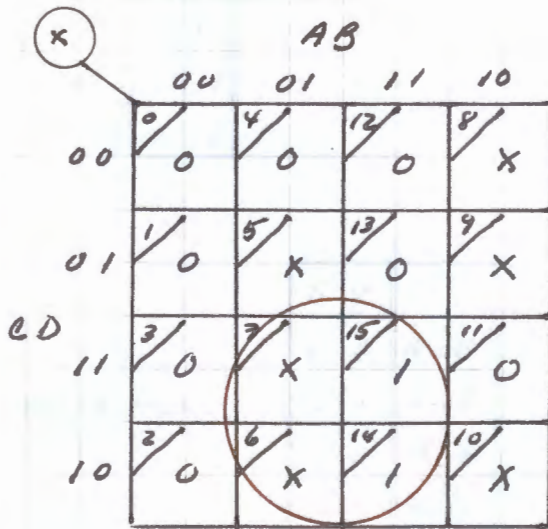
- 1).- BINARIO
- 2).- BCD
- 3).- EXCESO
- 4).- 2421
- 5).- GRAY

2421 \rightarrow GRAY



2421 \rightarrow 10 \rightarrow GRAY

\downarrow 10	M	A	B	C	D	X	Y	Z	W
0	0	0	0	0	0	0	0	0	0
1	1	0	0	0	1	0	0	0	1
2	2	0	0	1	0	0	0	1	1
3	3	0	0	1	1	0	0	1	0
4	4	0	1	0	0	0	1	1	0
	5	0	1	0	1	X	X	X	X
	6	0	1	1	0	X	X	X	X
	7	0	1	1	1	X	X	X	X
	8	1	0	0	0	X	X	X	X
	9	1	0	0	1	X	X	X	X
	10	1	0	1	0	X	X	X	X
5	11	1	0	1	1	0	1	1	1
6	12	1	1	0	0	0	1	0	1
7	13	1	1	0	1	0	1	0	0
8	14	1	1	1	0	1	1	0	0
9	15	1	1	1	1	1	1	0	1



$\underline{\underline{A(X) = BC}}$ *

GRAY → 1421

BCD → 2421

V_{10}	M	A	B	C	D	M	X	Y	Z	W
0	0	0	0	0	0	0	0	0	0	0
1	1	0	0	0	1	0	0	0	0	1
3	2	0	0	1	0	0	0	0	1	1
2	3	0	0	1	1	0	0	0	0	0
7	4	0	1	0	0	0	1	1	0	1
6	5	0	1	0	1	0	1	1	0	0
4	6	0	1	1	0	0	1	0	0	0
5	7	0	1	1	1	0	1	0	1	1
15	8	1	0	0	0	1	1	0	1	1
14	9	1	0	0	1	1	0	1	0	0
12	10	1	0	1	0	1	0	0	1	0
13	11	1	0	1	1	1	0	0	1	1
8	12	1	1	0	0	0	1	1	1	0
9	13	1	1	0	1	0	1	1	1	1
14	14	1	1	1	0	1	0	0	0	1
10	15	1	1	1	1	1	0	0	0	0

V_{10}	M	A	B	C	D	X	Y	Z	W
0	0	0	0	0	0	0	0	0	0
1	1	0	0	0	1	0	0	0	1
2	2	0	0	1	0	0	0	1	0
3	3	0	0	1	1	0	0	1	1
4	4	0	1	0	0	0	1	0	0
5	5	0	1	0	1	1	0	1	1
6	6	0	1	1	0	1	1	0	0
7	7	0	1	1	1	1	1	0	1
8	8	1	0	0	0	1	1	1	0
9	9	1	0	0	1	1	1	1	1
10	10	1	0	1	0	X	X	X	X
11	11	1	0	1	1	X	X	X	X
12	12	1	1	0	0	X	X	X	X
13	13	1	1	0	1	X	X	X	X
14	14	1	1	1	0	X	X	X	X
15	15	1	1	1	1	X	X	X	X

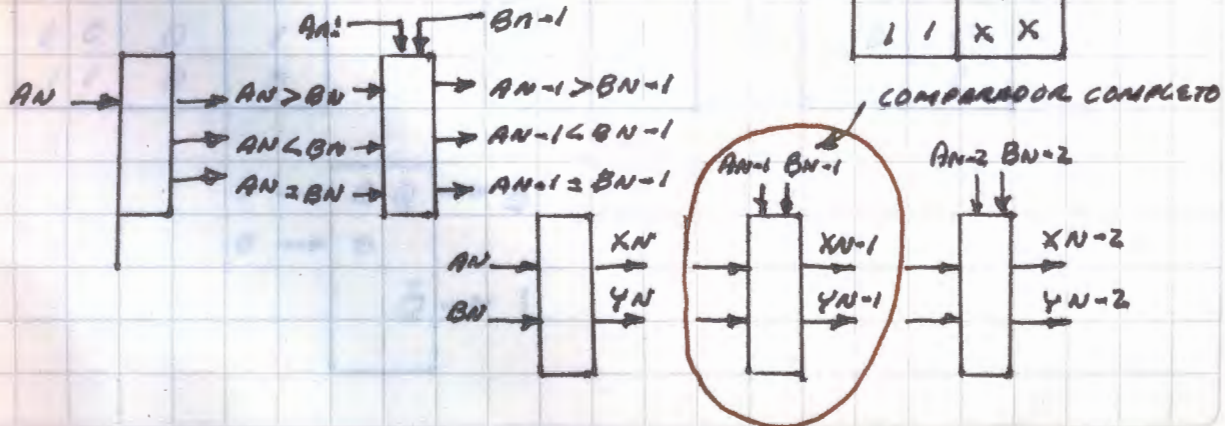
COMPARADOR COMPLETO

A 2, 3, 4, 6

B 2, 3, 4, 5

→ DE MAYOR PESO A MENOR PESO

X	Y	
0	0	A=B
0	1	A<B
1	0	A>B
1	1	X X



W	X	Y	A	B	X ₁	Y ₁	
0	0	0	0	0	0	0	1
1			0	1	0	1	0
2			1	0	1	0	0
3			1	1	0	0	1
4	0	1	0	0	0	1	0
5			0	1	0	1	0
6			1	0	0	1	0
7			1	1	0	1	0
8	1	0	0	0	1	0	0
9			0	1	1	0	0
10			1	0	1	0	0
11			1	1	1	0	0
12	1	1	0	0	X	X	X
13			0	1	X	X	X
14			1	0	X	X	X
15			1	1	X	X	X

"FLIP FLOPS"
- MEMORIA CON CAPACIDAD
DE UN SOLO BIT

FFs {
R-S → RESET-SET
J-K →
D →
T → TRIGGER
S-C → SET-CLEAR

FLIP-FLOPS "R-S"
RESET-SET

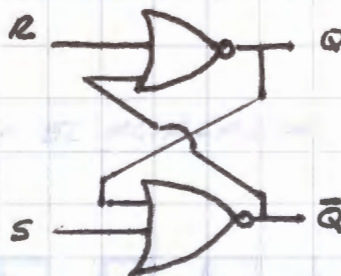
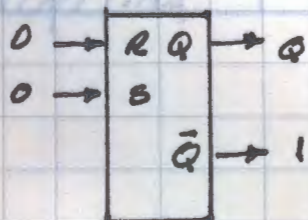


TABLA CARACTERÍSTICA

R	S	Q _{n+1}	\bar{Q}_{n+1}
0	0	0	1
0	0	1	0
0	1	1	0
1	0	0	1
1	1	0	0

A	B	$\bar{A+B}$
0	0	1
0	1	0
1	0	0
1	1	0

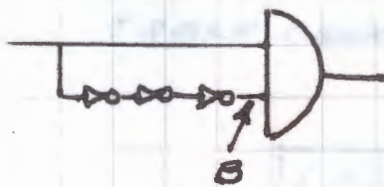
R	S	Q _{n+1}
0	0	Q _n
0	1	1
1	0	0
1	1	?



(R-S) RS CON RELOJ

CK = RELOJ

CP = RELOJ DE PULSO



SEÑAL DE SINCRONIA

TP = TIEMPOS DE PROPAGACION

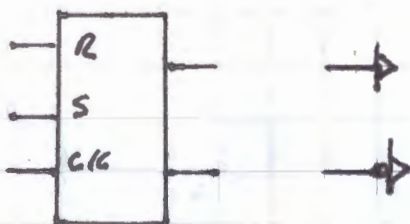
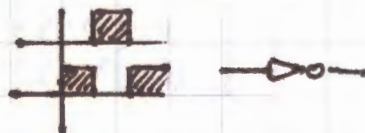
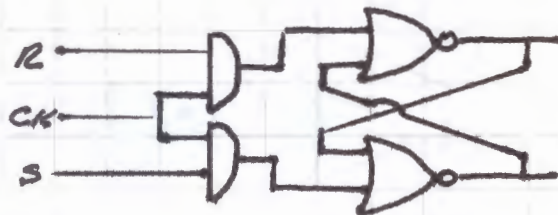
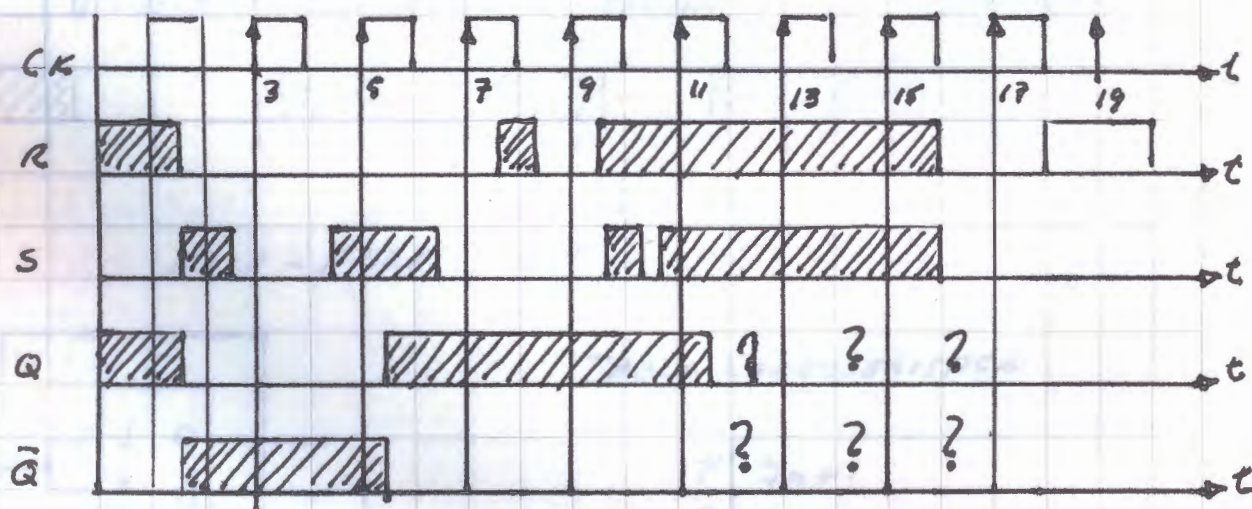
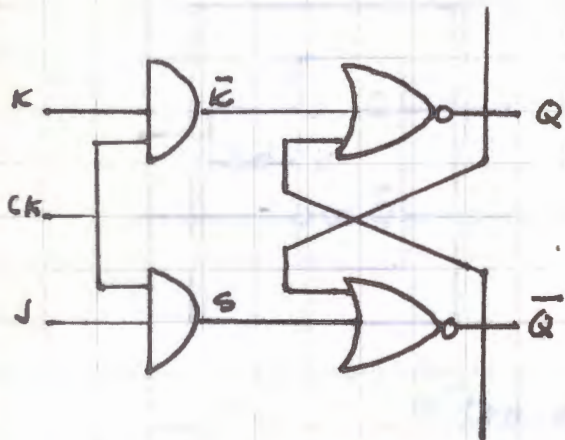


DIAGRAMA DE TIEMPOS PARA EL FLIP-FLOP R S

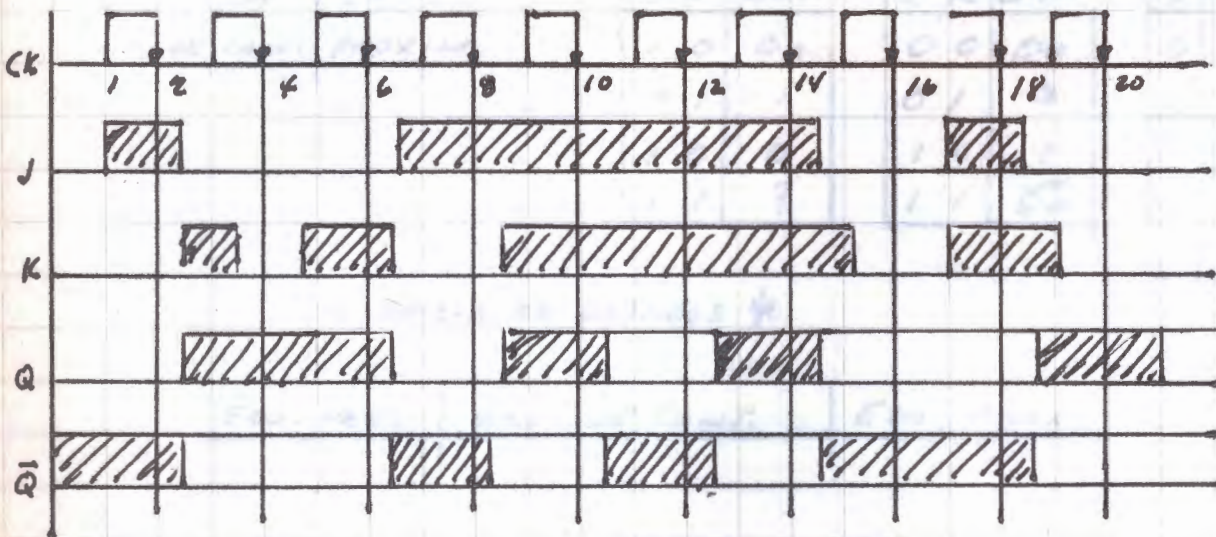


FLIP-FLOP

TABLA CARACTERISTICA

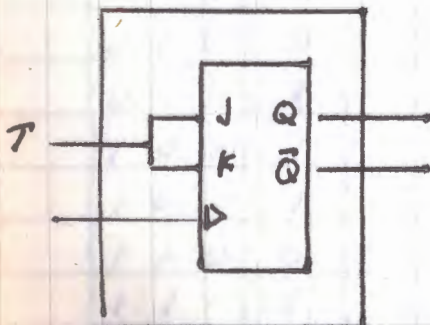


J	K	Q_{n+1}
0	0	Q_n
0	1	0
1	0	1
1	1	\bar{Q}_n



FLIP-FLOP "T"

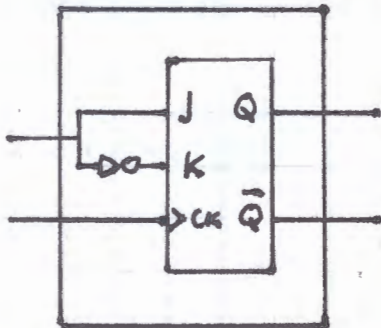
TABLA CARACTERISTICA



T	Q_{n+1}
0	Q_n
1	\bar{Q}_n

FLIP-FLOP "D"

"TABLA CARACTERISTICA"



D	Q_{n+1}
0	0
1	1

"TABLAS DE LOS FLIPS FLOPS"

TABLA CARACTERISTICA

ENT. DE CONT.	ENDO PROXIM
---------------	-------------

R	S	Q_{n+1}
0	0	Q_n
0	1	1
1	0	0
1	1	?

J	K	Q_{n+1}
0	0	Q_n
0	1	0
1	0	1
1	1	$\overline{Q_n}$

D	Q_{n+1}
0	0
1	1
T	Q_{n+1}
0	Q_n
1	$\overline{Q_n}$

* TABLA DE ESTADOS *

EDO. PRES	ENT. DE CONT.	EDO. PROX.
-----------	---------------	------------

Q_n	R	S	Q_{n+1}
0	0	0	0
0	0	1	1
0	1	0	0
0	1	1	?
1	0	0	1
1	0	1	1
1	1	0	0
1	1	1	?

Q_n	J	K	Q_{n+1}
0	0	0	0
0	0	1	0
0	1	0	1
0	1	1	1
1	0	0	1
1	0	1	0
1	1	0	1
1	1	1	0

Q_n	T	Q_{n+1}
0	0	0
0	1	1
1	0	1
1	1	0

Q_n	D	Q_{n+1}
0	0	0
0	1	1
1	0	0
1	1	1

* TABLA DE EXILACION *

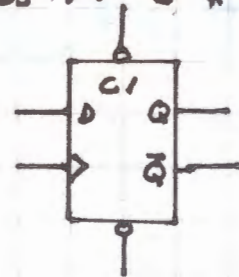
EQO. PRES. EQO. PROX | ENT. DE CONT.

$Q_n Q_{n+1}$	R S	J K	T	D
00	X 0	0 X	0	0
01	0 1	1 X	1	1
10	1 0	X 1	1	0
11	0 X	X 0	0	1

* OTRAS ENTRADAS DE CONTROL A LOS FF'S *

CLEAR - CL $\overline{0}$ | cl $\overline{1}$ | cl $Q=0$

PRESET - Pr $\overline{0}$ | pr $\overline{1}$ | pr $Q=1$



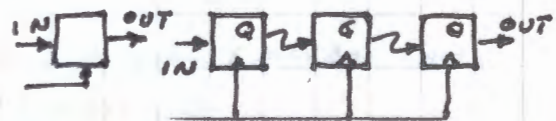
CL	Pr	CK	D	Q	\overline{Q}
L	H	X	X	L	H
H	L	X	X	H	L
L	L	X	X	H	H
H	H	↑	H	H	L
H	H	↑	L	L	H
H	H	L	X	Q	\overline{Q}

H = HIGH = 1
 L = LOW = 0
 X = NO IMPORTA

REGISTROS DE CORRIENTES

a) SISO

SERIAL IN SERIAL OUT



b) SIPO

c) SIPO



d) PIPO

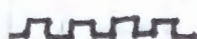


* MULTIBIBRADORES *

a) BISTABLE



c) ESTABLE



b) MONOESTABLE

1.- REDISPARRABLE

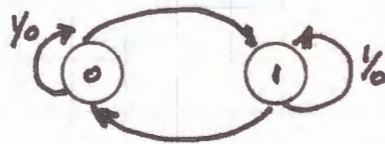
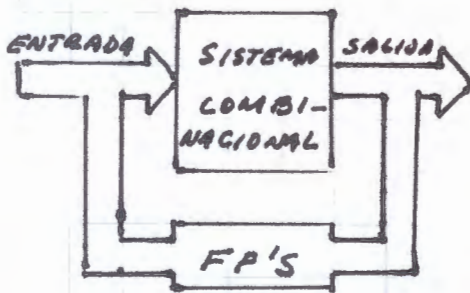
1.- NO REDISPARRABLE

REGISTRO DE CORRIENTE UNIVERSAL: PUEDE CONVERTIRSE EN:
SISO; SIPO; PISO; PIPO.

*** DISEÑO SECUENCIAL ***

* SISTEMA SECUENCIAL: → ES AQUEL QUE LOS VALORES DE SALIDA NO SOLO DEPENDEN DE LAS COMBINACIONES DE ENTRADA SINO QUE TAMBIEN DE LAS SALIDAS MISMAS

1).- PASIVOS 2).- ACTIVOS *) DIAGRAMA DE TRANSICION *



ENTRADA
N/O SALIDA.

*** TABLA DE ESTADO ***

ESTADOS PRESENTES	ESTADOS PROXIMOS	ESTADOS DE CAMBIO			SALIDA
Qn Qn-1	Qn+1	Jn Kn			D
0 0	1	1 X			0
0 1	0	X 1			1
1 0	0	0 X			0
1 1	1	X 0			0

* PARA LLENAR LOS ESTADOS PROXIMOS A LAS SALIDAS NOS BASAMOS EN EL DIAGRAMA DE TRANSICION.

* PARA OBTENER LOS VALORES DE LAS ENTRADAS DE CONTROL, COMPARAMOS LOS VALORES DE LOS ESTADOS PRESENTES Y ESTADOS PROXIMOS CON LA TABLA DE EXITACION DEZ PPT CORRESPONDIENTE Y OBTENEMOS LAS ENTRADAS DE CONTROL

N	QA	JA	KA	D

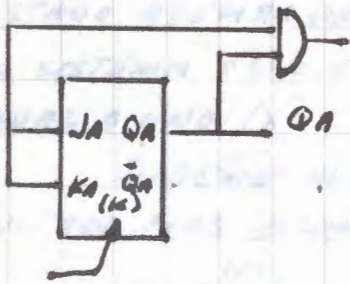
	N	QA
JA	0	1
QA	1	X

$$FJA = \bar{N}$$

	N	QA
KA	0	1
QA	1	0

$$KA = \bar{N}$$

$$JA = KA = \bar{N}$$



* → D

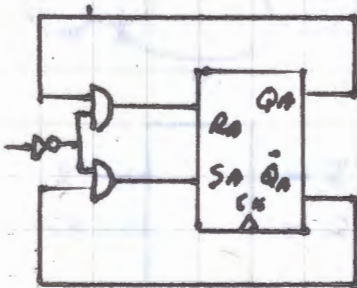
RA	SA
0	1
1	0
X	0
0	X

	N	QA
RA	0	1
QA	0	X
	1	0

$$RA = \bar{N}QA$$

	N	QA
SA	0	1
QA	1	0
	1	X

$$SA = \bar{N}QA$$



* METODOLOGIA DEL DISEÑO SECUENCIAL *

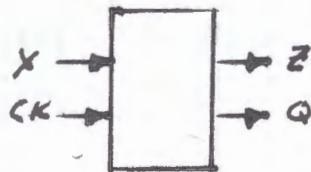
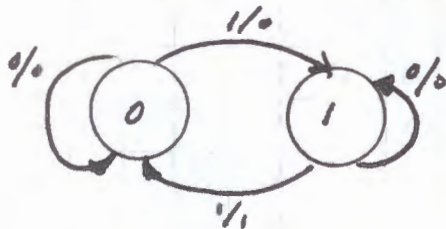
- 1.- ESPECIFICAR EL SISTEMA MEDIANTE UN DIAGRAMA DE TRANSICION
- 2.- ENUMERAR LAS ENTRADAS Y SALIDAS
- 3.- DETERMINAR EL NUMERO DE FF'S ASI COMO SELECCIONAR EL TIPO O TIPOS DE FF'S
- 4.- TRASLADAR EL COMPORTAMIENTO DEL SISTEMA A UNA TABLA DE ESTADOS
- 5.- OBTENER LAS FUNCIONES DEL SISTEMA COMBINACIONAL USANDO MAPAS DE KARNAUGH
- 6.- IMPLEMENTAR

EJEMPLO N°1

DISEÑE UN SISTEMA SECUENCIAL DE DOS ESTADOS QUE CONTENGAN UNA ENTRADA (X) DE MODO QUE:

SI $(X=1)$ EL SISTEMA DEBERA DE CAMBIAR DE ESTADO SI $(X=0)$ EL SISTEMA DEBERA PERMANECER EN EL MISMO ESTADO, ADEMÁS UNA SALIDA Z QUE SEA IGUAL A $(Z=1)$ SI EL SISTEMA ESTA EN ESTADO (1) $(Q=1)$ Y LA ENTRADA IGUAL A UNO $(X=1)$

DISEÑE CON TODOS LOS FF'S, Y ESCOJA EL RESULTADO MAS ECONOMICO.

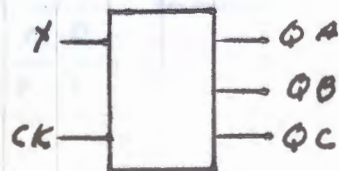
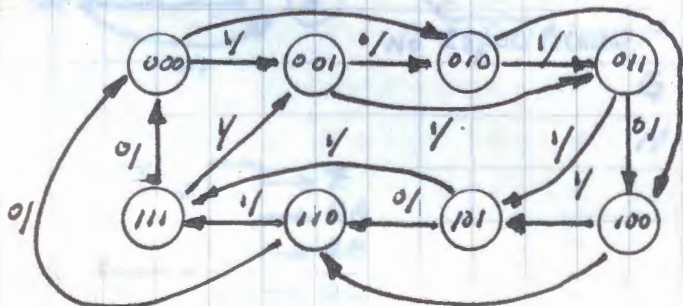


ESTADOS PRESENTES	ESTADOS PROXIMOS	ENTRADAS DE CONTROL					SALIDA
		R	S	J	K	T	
Q	Q+1						
0 0	0	K	0	0	X	0 0	0
0 1	1	0	X	X	0	0 1	0
1 0	1	0	1	1	X	1 1	0
1 1	0	1	0	X	1	1 0	1

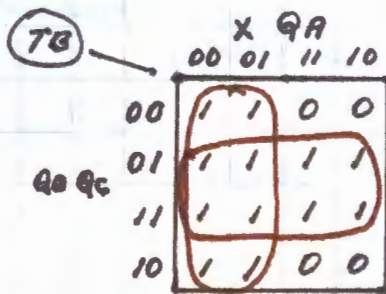
DISEÑE UN SISTEMA SECUENCIA DE (P) ESTADOS CON UNA ENTRADA (X) DE MODO QUE: SI $(X=0)$ LA SECUENCIA DEBERA SER POR LOS ESTADOS PARES. SI $(X=1)$ LA SECUENCIA DEBERA SER POR LOS ESTADOS IMPAR.

ADEMÁS SI EL SISTEMA ESTA EN ESTADO ^{IMPAR} PARES $(X=1)$ EL ESFUERZO PROXIMO SERA EL IMPAR CONSECUTIVO Y ^{X=0} USUARIA.

* EL ESTADO CERO (0) ES CONSIDERADO COMO ^{PAR} PARES.



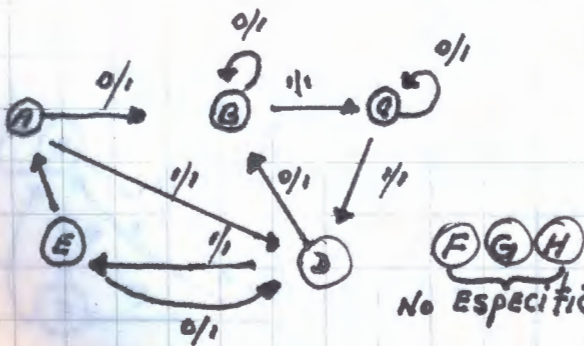
ESTADOS PRESENTES				ESTADOS PROXIMOS			ENTRADAS DE CONTROL		
X	QA	QB	QC	QA+1	QB+1	QC+1	TA	TB	TC
0	0	0	0	0	1	0	0	1	0
0	0	0	1	0	1	0	0	1	1
0	0	1	0	1	0	0	1	1	0
0	0	1	1	1	0	0	1	1	1
0	1	0	0	1	1	0	0	1	0
0	1	0	1	1	1	0	0	1	1
0	1	1	0	0	0	0	1	1	0
0	1	1	1	0	0	0	1	1	1
1	0	0	0	0	0	1	0	0	1
1	0	0	1	0	1	1	0	1	0
1	0	1	0	0	1	1	0	0	1
1	0	1	1	1	0	1	1	1	0
1	1	0	0	1	0	1	0	0	1
1	1	0	1	1	1	1	0	1	0
1	1	1	1	0	0	1	1	1	0



$$TB = \bar{X} + QC *$$

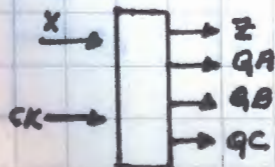
12/NOV/85

* SISTEMAS SECUENCIALES QUE NO ESTAN COMPLETAMENTE ESPECIFICADOS.



- A = 0 0 0
- B = 0 0 1
- C = 0 1 0
- D = 0 1 1
- E = 1 0 0
- F = 1 0 1
- G = 1 1 0
- H = 1 1 1

X=0	X=1
A→B	A→D
B→B	B→C
C→C	C→D
D→B	D→E
E→D	E→A.



ES	ESTADOS PRESENTES				ESTADOS PROXIMOS			ENTRADAS DE CONTROL			SALIDA Z
	X	QA	QB	QC	QA+1	QB+1	QC+1	TA	TB	TC	
A	0	0	0	0	0	0	1	0	0	1	1
B	0	0	0	1	0	0	1	0	0	0	1
C	0	0	1	0	0	1	0	0	0	0	1
D	0	0	1	1	0	0	1	0	1	0	1
E	0	1	0	0	0	1	1	1	1	1	1
F	0	1	0	1	X	X	X	X	X	X	X
G	0	1	1	0	X	X	X	X	X	X	X
H	0	1	1	1	X	X	X	X	X	X	X
A	1	0	0	0	0	1	1	0	1	1	1
B	1	0	0	1	0	1	0	0	1	1	1
C	1	0	1	0	0	1	1	0	0	1	1
D	1	0	1	1	1	0	0	1	1	1	1
E	1	1	0	0	0	0	0	1	0	0	0
F	1	1	0	1	X	X	X	X	X	X	X
G	1	1	1	0	X	X	X	X	X	X	X
H	1	1	1	1	X	X	X	X	X	X	X

TC

	X QA			
	00	01	11	10
QA QC	1 1 0 1	0 X X 1	0 X X 1	0 X X 1

$$TC = \bar{x} \bar{q}_b \bar{q}_c + x \bar{q}_a$$

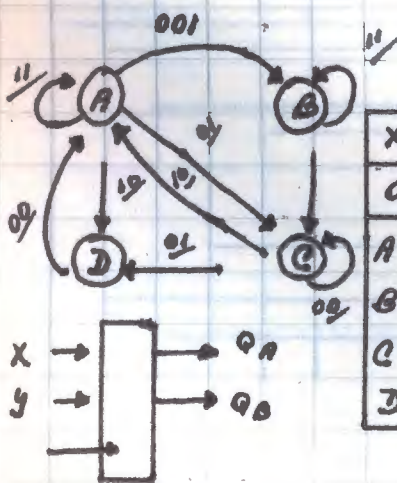
TB

	X QA			
	00	01	11	10
QA QC	0 1 0 1	0 X X 1	1 X X 1	0 X X 0

$$TB = \bar{x} q_a + q_b q_c + x \bar{q}_a \bar{q}_b$$

13/Nov/85

* SISTEMAS SECUENCIALES DE ENTRADAS MÚLTIPLES *

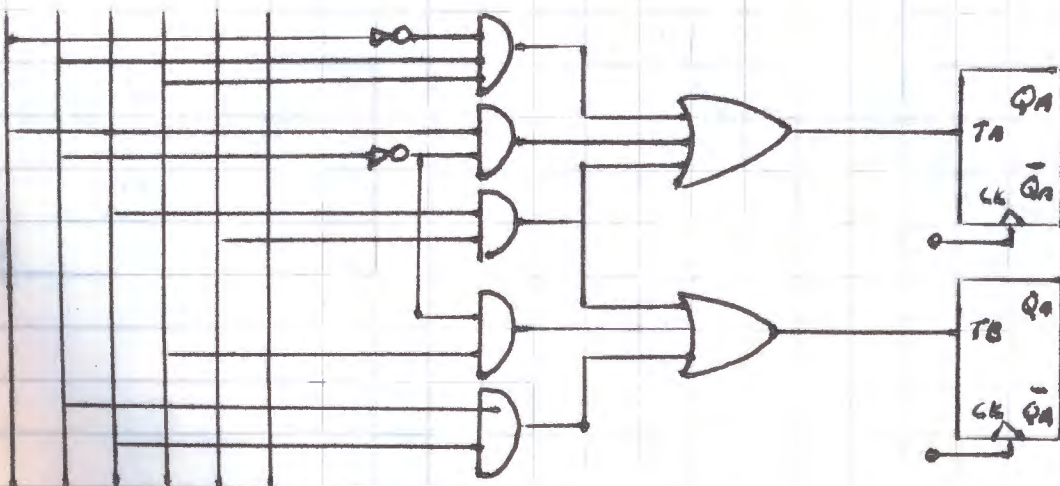
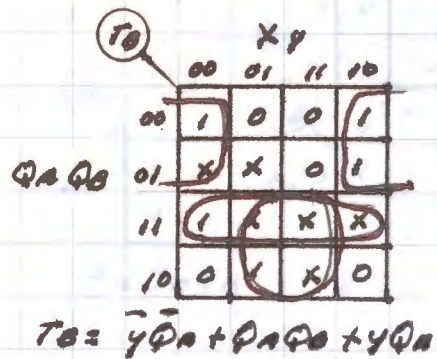
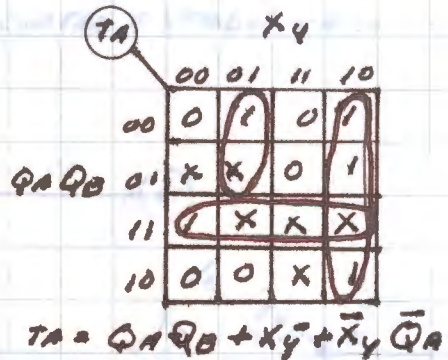


X	Y	X	Y	X	Y	X	Y	
0	0	0	1	1	0	1	1	
A	→	B	A	→	C	A	→	A
B	→	X	B	→	X	B	→	C
C	→	C	C	→	D	C	→	A
D	→	A	D	→	A	D	→	X

DANDOLE VALORES A LOS VARIABLES

- A → 0 0
- B → 0 1
- C → 1 0
- D → 1 1

Es	ESTADOS PRESENTES		ESTADOS PROXIMOS		ENTRADAS DE CONTROL		
	X	Y	QAQB	QA+1	QB+1	TA	TB
A	0	0	0 0	0	1	0	1
B			0 1	X	X	X	X
C			1 0	1	0	0	0
D			1 1	0	0	1	1
A	0	1	0 0	1	0	1	0
B			0 1	X	X	X	X
C			1 0	1	1	0	1
D			1 1	X	X	X	X
A	1	0	0 0	1	1	1	1
B			0 1	1	0	1	1
C			1 0	0	0	1	0
D			1 1	X	X	X	X
A	1	1	0 0	0	0	0	0
B			0 1	0	1	0	0
C			1 0	X	X	X	X
D			1 1	X	X	X	X



ELLI

* CONTADORES *

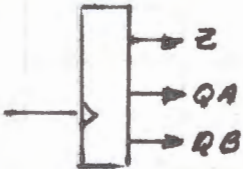
TIPOS DE CONTADORES

1.- BINARIO

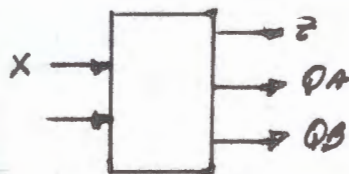
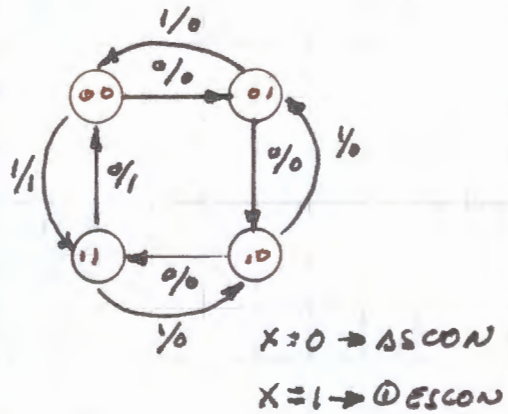
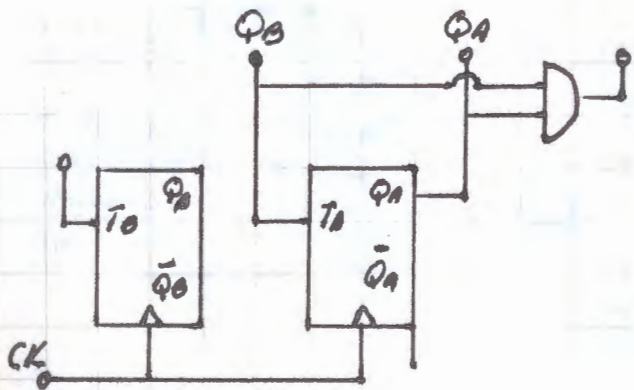
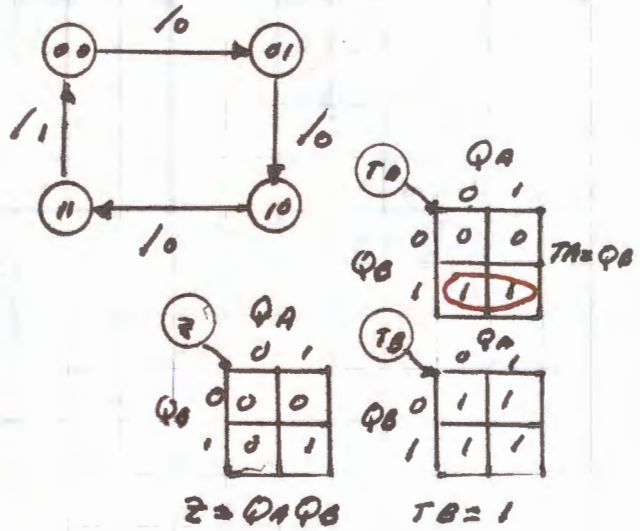
2.- DECADAS, BCD, DECIMALES

3.- CONTADORES ESPECIALES

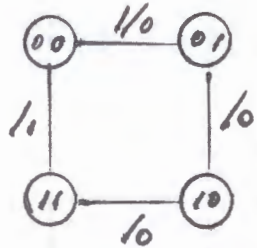
DISEÑO DE UN CONTADOR EN BINARIO NATURAL DE CUATRO ESTADOS Y ASCENDENTE (MODULO 4)



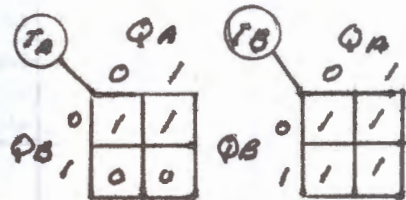
ESTADOS PRESENTES		ESTADOS PROXIMOS		ENTRADAS DE CONTROL		SAIDA
QA	QB	QA+1	QB+1	TA	TB	Z
0	0	1	0	1	1	0
0	1	0	1	1	1	0
1	0	1	0	1	1	0
1	1	0	0	1	1	1



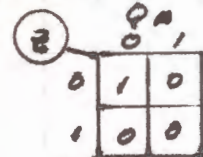
* DESCENDENTE *



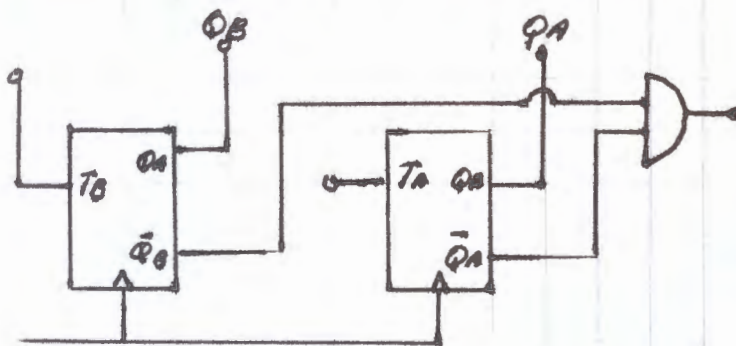
QA	QB	QAH	QBH	TA	TB	Z
0	0	1	1	1	1	1
0	1	0	0	0	1	0
1	0	0	1	1	1	0
1	1	1	0	0	1	0



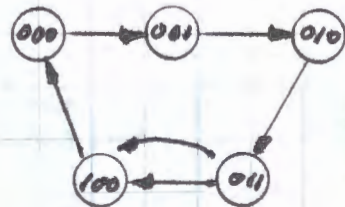
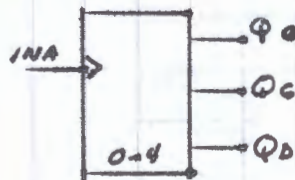
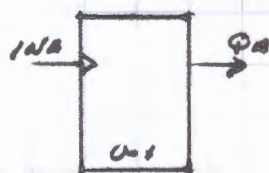
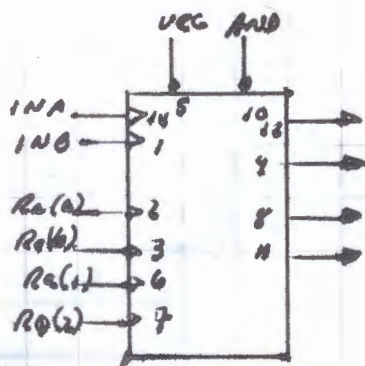
$TA = \bar{Q}_B$ $TB = 1$



$Z = \bar{Q}_A \bar{Q}_B$



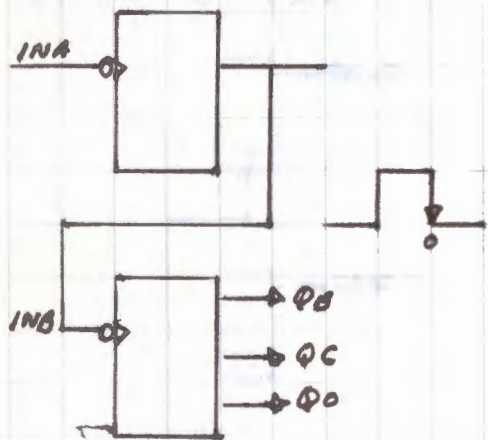
SN 7490



EN SN 7490 COMO CONTADOR DE DÉCADAS

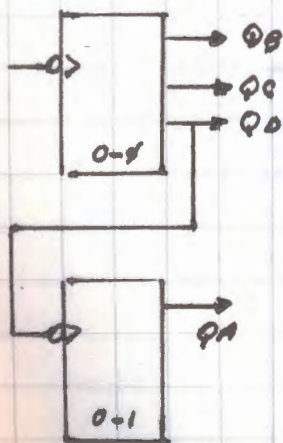


EN SN 7490 COMO CONTADOR DE DECADAS

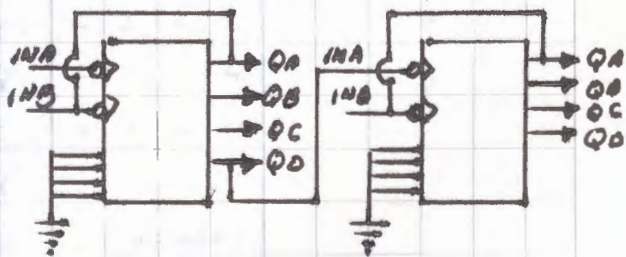


	0-4	0-1		
No	Q ₀	Q _c	Q _B	Q _A
0	0	0	0	0
1	0	0	0	1
2	0	0	1	0
3	0	0	1	1
4	0	1	0	0
5	0	1	0	1
6	0	1	1	0
7	0	1	1	1
8	1	0	0	0
9	1	0	0	1
0	0	0	0	0
1	0	0	0	1
2	0	0	1	0
3	0	0	1	1

SN 7490 CONTADO EN

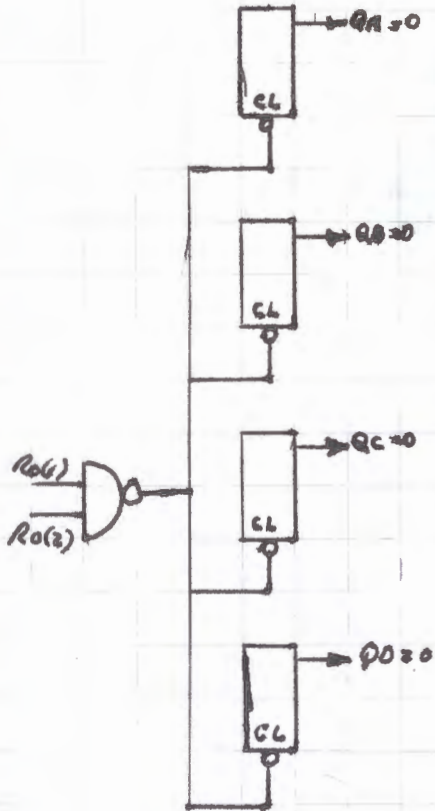


	0-4	0-1		
No	Q ₀	Q _c	Q _B	Q _A
0	0	0	0	0
2	0	0	1	0
4	0	1	0	0
6	0	1	1	0
3	1	0	0	0
1	0	0	0	1
3	0	0	1	1
5	0	1	0	1
7	0	1	1	1
9	1	0	0	1
0	0	0	0	0

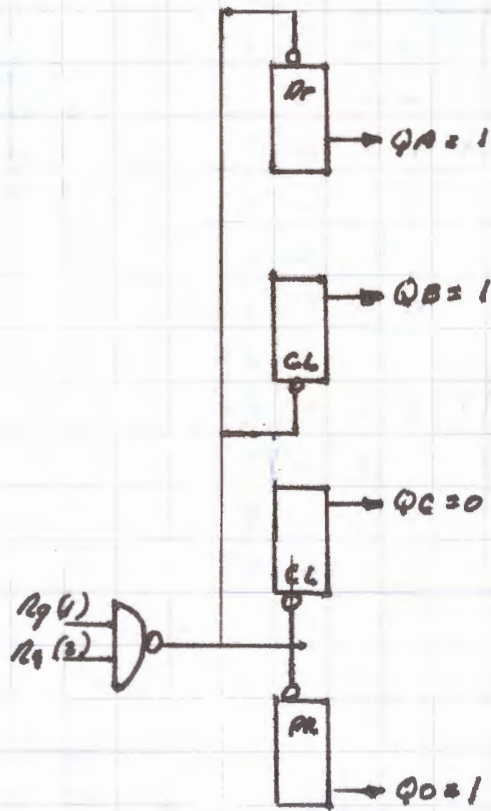


RESETS

RESET A CERO



RESET A NUEVO

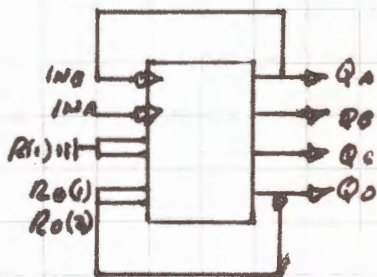


RQ(1)	RQ(2)	RQ(1)	RQ(2)	Q _D	Q _C	Q _B	Q _A
X	L	H	H	L	L	L	L
L	X	H	H	L	L	L	L
H	H	X	X	H	L	L	H
L	X	L	X				
L	X	X	L	CONTADOS			
X	L	X	L				
X	L	L	X				

* CONTADORES PROGRAMABLES *

0 → X

a) 0 → 3



M	Q ₀	Q ₁	Q ₂	Q ₃
0	0	0	0	0
1	0	0	0	1
2	0	0	1	0
3	0	0	1	1
4	0	1	0	0
5	0	1	0	1
6	0	1	1	0
7	0	1	1	1
8	1	0	0	0
9	1	0	0	1
0	0	0	0	0

b) 0 → 47

